

NX8102 MB V1.0 LPDDR4

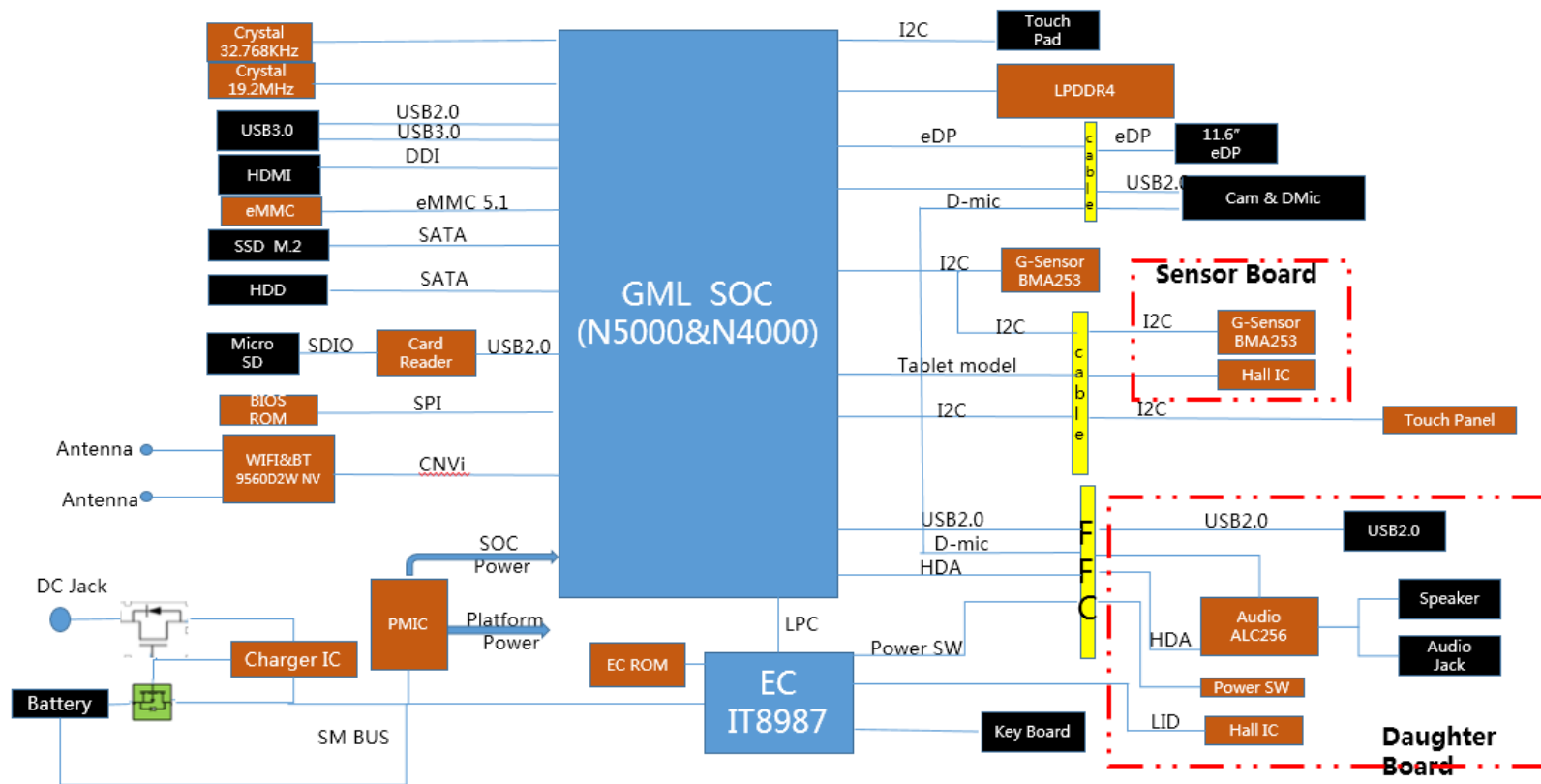
Intel Gemini Lake Platform

| | |
|------------------------------------|-----------------------------|
| 01--COVER PAGE | 26--SENSORS |
| 02--BLOCK DIAGRAM | 27--TOUCH PANEL&SENSOR |
| 03--POWER BLOCK DIAGRAM | 28--SSD |
| 04--SYSTEM LIST | 29--WLAN |
| 05--GLK_SOC_MEM(CHA) | 30--USB3.0 |
| 06--GLK_SOC_MEM(CHB) | 31--EC ITE8987 |
| 07--GLK_SOC_(EMMC/SPI/LPC/AUD/PMU) | 32--EC ROM &THERMAL SENSOR |
| 08--GLK_SOC(DDI/DSI) | 33--HDD |
| 09--GLK_SOC(I2C/CNV/LPSS/SPI/UART) | 34--POWER BUTTON |
| 10--GLK_SOC(PCIE/SATA/USB) | 35--KEY BOARD&TOUCH PAD |
| 11--GLK_SOC(JTAG/GPIO) | 36--IO BOARD CONN |
| 12--GLK_SOC(PWR) | 37--DCIN&CHG LED&DEBUG CONN |
| 13--GLK_SOC(GND) | 38--CHARGE |
| 14--GLK_SOC(DECAP) | 39--POWER DELIVERY 3P3V |
| 15--GLK_SOC(STRAP) | 40--POWER DELIVERY 5P5V |
| 16--MEM_LPDDR4_CHA0 | 41--PMIC RT5077_1 |
| 17--MEM_LPDDR4_CHA1 | 42--PMIC RT5077_2 |
| 18--MEM_LPDDR4_CHB0 | 43--POWER DELIVERY VDDQ |
| 19--MEM_LPDDR4_CHB1 | 44--LOAD SWITCH |
| 20--LPDDR4_DECAPS | 45--HOLE&BOSS |
| 21--SYSTEM_SPI_FLASH | 46--CHANGE LIST |
| 22--HDMI | 47-- |
| 23--EDP | 48-- |
| 24--EMMC | 49-- |
| 25--SD CRAD READER | 50-- |



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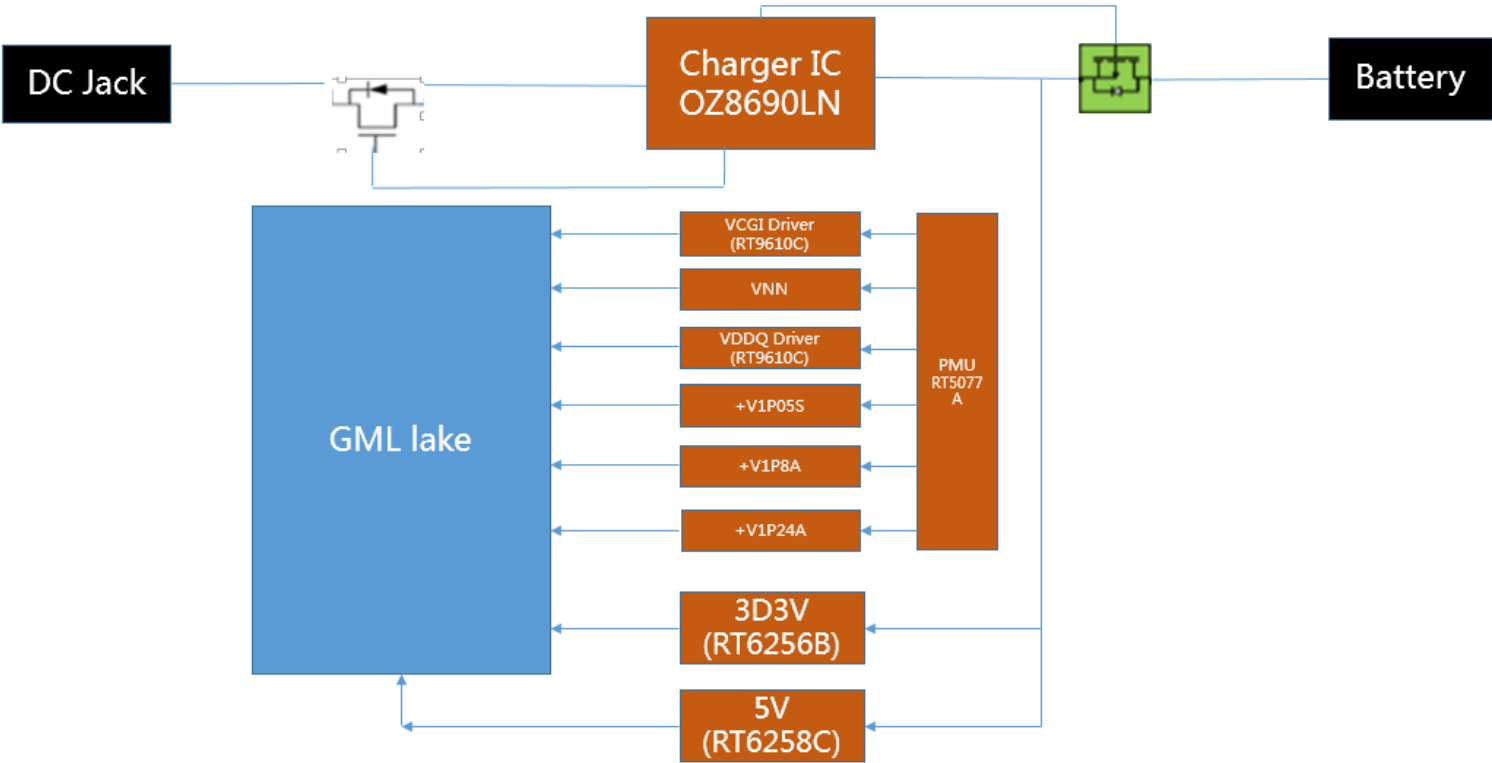
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Power States(Adapter)

| Signal | SLP_S0# | SLP_S3# | SLP_S4# | +V5P0A | +V3P3A | VNN | V1P8A | V1P2A | VCGI | +V1P05S | +VDDQ |
|--------------|---------|---------|---------|--------|--------|-----|-------|-------|------|---------|-------|
| S0(Full On) | HIGH | HIGH | HIGH | ON | ON | ON | ON | ON | ON | ON | ON |
| S3 (STM) | LOW | LOW | HIGH | ON | ON | OFF | ON | ON | OFF | OFF | ON |
| S4 (STD) | LOW | LOW | LOW | ON | ON | OFF | ON | ON | OFF | OFF | OFF |
| S5 (SoftOff) | LOW | LOW | LOW | ON | ON | OFF | ON | ON | OFF | OFF | OFF |

Power States(Battery)

| Signal | SLP_S0# | SLP_S3# | SLP_S4# | +V5P0A | +V3P3A | VNN | V1P8A | V1P2A | VCGI | +V1P05S | +VDDQ |
|--------------|---------|---------|---------|--------|--------|-----|-------|-------|------|---------|-------|
| S0(Full On) | HIGH | HIGH | HIGH | ON | ON | ON | ON | ON | ON | ON | ON |
| S3 (STM) | LOW | LOW | HIGH | ON | ON | OFF | ON | ON | OFF | OFF | ON |
| S4 (STD) | LOW | LOW | LOW | OFF | OFF | OFF | OFF | OFF | OFF | OFF | OFF |
| S5 (SoftOff) | LOW | LOW | LOW | OFF | OFF | OFF | OFF | OFF | OFF | OFF | OFF |

USB2.0 Port

| Port | Device |
|------|---------------|
| 0 | USB2.0 Type-A |
| 1 | USB3.0 Type-A |
| | |
| 5 | Card Reader |
| 6 | Camera |
| | |

MB ID

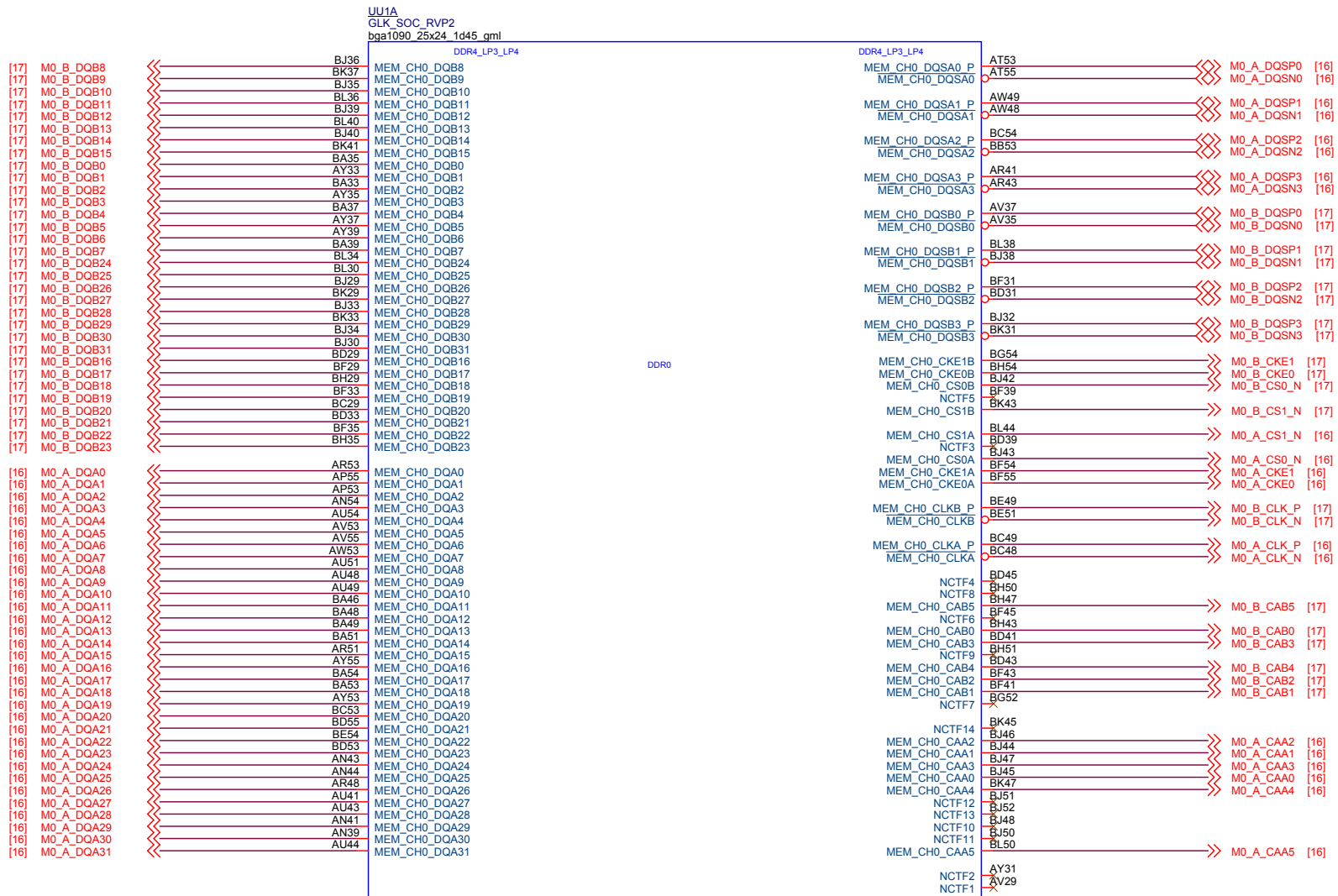
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|-----|--------------|--------------|--------------|--------------|--------------|--------------|-------------|
| EVT | X | X | 0 | 1 | 1 | X | X |
| | | | | | | | |
| | | | | | | | |
| | | | | | | | |
| | | | | | | | |

MEM ID

| MEM_ID2 | MEM_ID1 | MEM_ID0 | |
|---------|---------|---------|--|
| 0 | 0 | 0 | 2x Micron 1GB (MT53B256M32D1NP-062) |
| 0 | 0 | 1 | 4x Micron 1GB (MT53B256M32D1NP-062) |
| 0 | 1 | 0 | 4x Hynix 1GB (H9HCNNN8KUMLHR-NME) |
| 0 | 1 | 1 | 2x Hynix 1GB (H9HCNNN8KUMLHR-NME) |
| 1 | 0 | 0 | 2X SK Hynix 2GB (H9HCNNNBPUMLHR-NME) |
| 1 | 0 | 1 | 2X Samsung 2GB (K4F6E3S4HM-MGCJ) |
| 1 | 1 | 0 | 2X Micron 2GB (MT53E512M32D2NP-046 WT:E) |

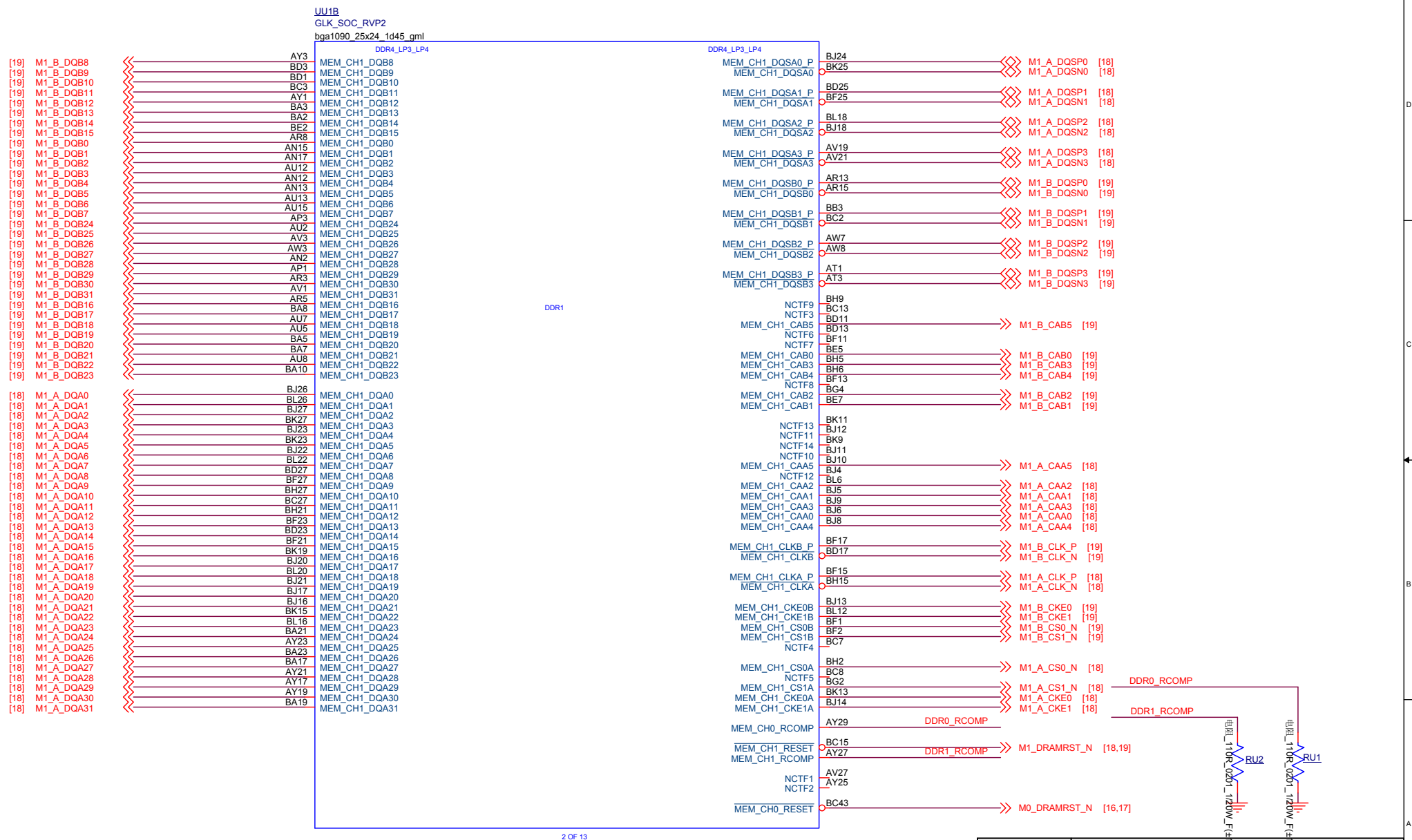


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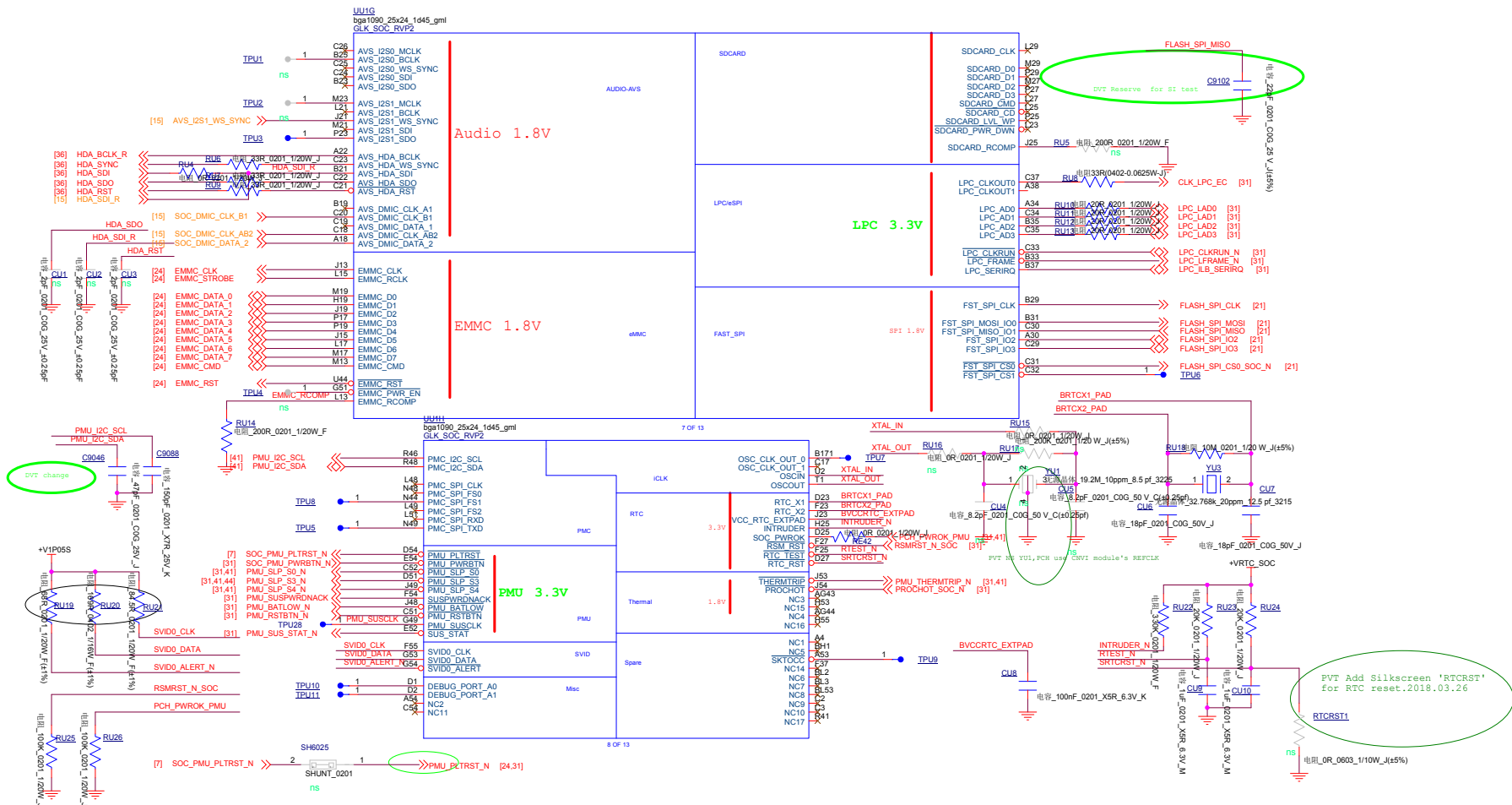
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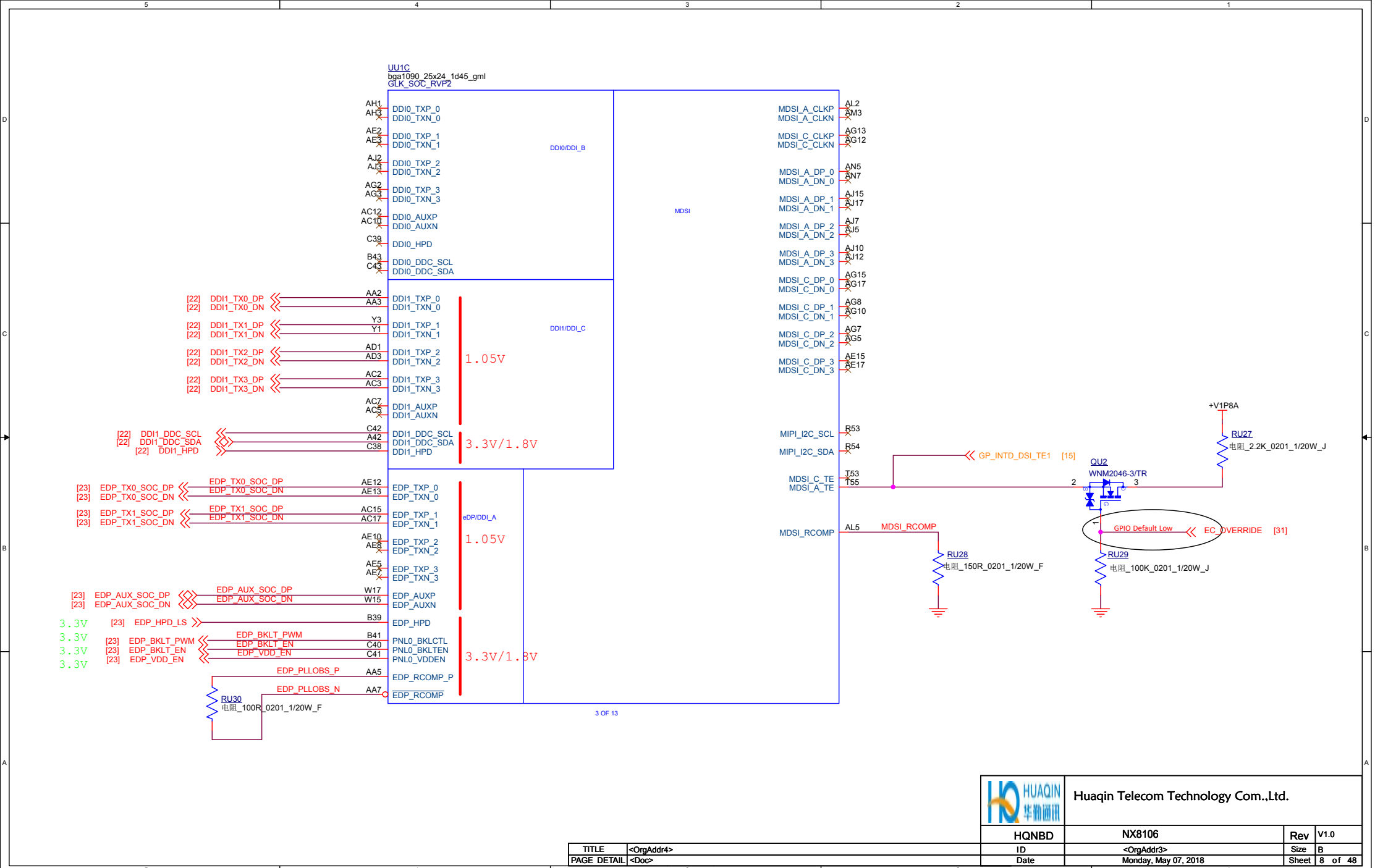
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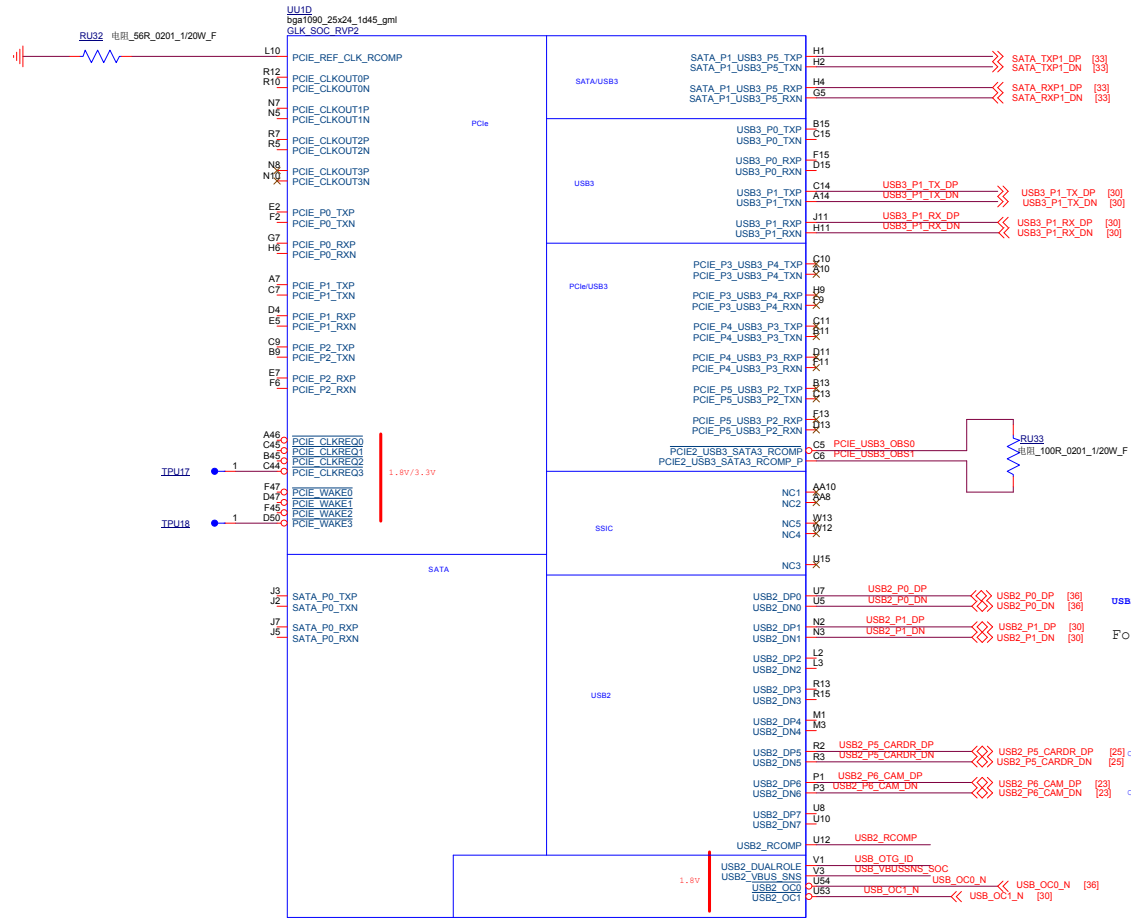
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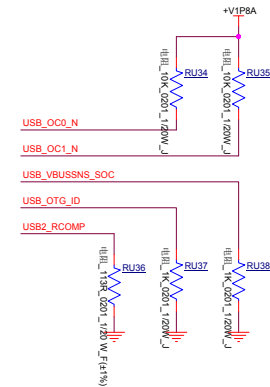
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WLAN



HDD

Type A



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bga1090_25x24_1d45_gml
GLK_SOC_RVP2
IS

TPU19 1 AH53 JTAGX
TPU20 1 AH54 JTAG_TCK
TPU21 1 AL53 JTAG_TDI
TPU22 1 AL54 JTAG_TDO
TPU23 1 AK53 JTAG_TMS
TPU24 1 AK53 JTAG_TRST

TPU26 1 AH55 JTAG_PDV
TPU27 1 AJ53 JTAG_PREG

ITP

GPI

1.8V

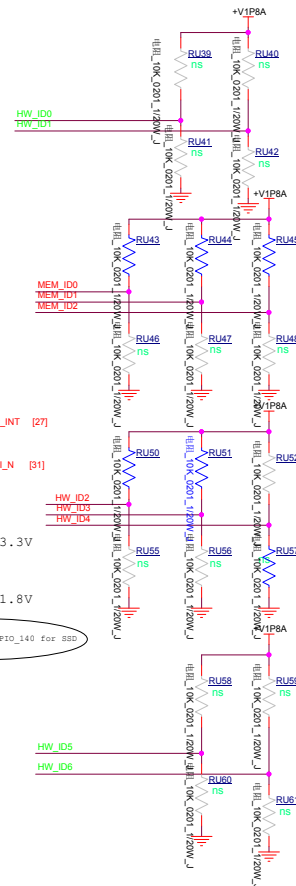
1.8V/3.3V

1.8V

GPIO_8 A253
GPIO_9 A554
GPIO_10 A554
GPIO_11 A555
GPIO_12 A553
GPIO_13 A554
GPIO_14 A553
GPIO_15 A553
GPIO_16 A553
GPIO_17 AA49
GPIO_18 AC48
GPIO_19 AC46
GPIO_20 AE51 MEM_ID0
GPIO_21 AE49 MEM_ID1
GPIO_22 AE51 MEM_ID2
GPIO_23 AC49
GPIO_24 AA51
GPIO_25 AA48
GPIO_26 AE41
GPIO_27 AE39
GPIO_28 AE46
GPIO_29 AE44
GPIO_30 AC41
GPIO_31 AC39 HW_ID0
GPIO_32 AC44
GPIO_33 AC43
GPIO_34 AA44
GPIO_35 AA54
GPIO_36 AA53
GPIO_37 Y55
GPIO_38 Y53
GPIO_39 W54
GPIO_40 W53
GPIO_41 V53
GPIO_105 L46
GPIO_134 H45
GPIO_135 L43
GPIO_136 M43
GPIO_137 H43
GPIO_138 H43
GPIO_139 J43
GPIO_140 D43
GPIO_141 F43
GPIO_142 H41
GPIO_143 H41
GPIO_144 C39
GPIO_145 F41
GPIO_146 D27
GPIO_210 U43
GPIO_212 U41
GPIO_213 U39
GPIO_214 X39

PMU_AC_PRESENT [31]
PMU_INTR [41]
MEM_ID0
MEM_ID1
MEM_ID2
TCH_PANEL_RESET_N [27]
EMMC_BOOT_STRAP [15]
SPI_BOOT_STRAP [15]
HW_ID0
SENZ_INT [26]
SENZ_INT [27]
SOC_WAKE_S0LN [31]
PMU_WAKE_N [31]
BT_RF_KILL_N [29]
TOUCH_PAD_INT [35]
TOUCH_PAD_INT [35]
WIFI_DISABLE_N [29]
HW_ID1
HW_ID2
HW_ID3
HW_ID4
HW_ID5
HW_ID6
TCH_PANEL_INT_N [27]

TPU25
change ID3&ID4,because GPIO_140 for SSD



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UU1K
bga1090_25x24_1d45_gml
GLK SOC RVP2

| | | | |
|------|-------|------|--------|
| A3 | VSS6 | AF44 | VSS53 |
| A6 | VSS13 | AF45 | VSS54 |
| A12 | VSS1 | AF47 | VSS55 |
| A16 | VSS2 | AF48 | VSS56 |
| A20 | VSS3 | AF50 | VSS57 |
| A24 | VSS4 | AF52 | VSS58 |
| A28 | VSS5 | AF53 | VSS59 |
| A32 | VSS7 | AF55 | VSS60 |
| A36 | VSS8 | AG20 | VSS64 |
| A40 | VSS9 | AL21 | VSS87 |
| A44 | VSS10 | AG25 | VSS65 |
| A48 | VSS11 | AG29 | VSS66 |
| A51 | VSS12 | AG35 | VSS67 |
| AA12 | VSS14 | AG38 | VSS68 |
| AA13 | VSS15 | AR17 | VSS77 |
| AA15 | VSS16 | AJ13 | VSS69 |
| AA17 | VSS17 | AJ18 | VSS70 |
| AA21 | VSS18 | AJ25 | VSS71 |
| AA23 | VSS19 | AJ29 | VSS72 |
| AA25 | VSS20 | AJ36 | VSS73 |
| AA27 | VSS21 | AJ38 | VSS74 |
| AA35 | VSS22 | AJ39 | VSS75 |
| AA43 | VSS23 | AJ44 | VSS76 |
| AA48 | VSS24 | AK1 | VSS78 |
| AB1 | VSS25 | AK3 | VSS79 |
| AB3 | VSS26 | AK55 | VSS80 |
| AB55 | VSS27 | AL3 | VSS90 |
| AC8 | VSS33 | AL7 | VSS97 |
| AC13 | VSS28 | AL8 | VSS98 |
| AC23 | VSS29 | AL10 | VSS81 |
| AC25 | VSS30 | AL12 | VSS82 |
| AC27 | VSS31 | AL13 | VSS83 |
| AC29 | VSS32 | AL15 | VSS84 |
| AE18 | VSS34 | AL17 | VSS85 |
| AE23 | VSS35 | AL20 | VSS86 |
| AE25 | VSS36 | AL25 | VSS88 |
| AE27 | VSS37 | AL29 | VSS89 |
| AE43 | VSS38 | AL39 | VSS91 |
| AE48 | VSS39 | AL41 | VSS92 |
| AF1 | VSS40 | AL43 | VSS93 |
| AF3 | VSS49 | AL44 | VSS94 |
| AF4 | VSS50 | AL46 | VSS95 |
| AF6 | VSS61 | AL51 | VSS96 |
| AF8 | VSS62 | AM1 | VSS99 |
| AF9 | VSS63 | AM21 | VSS100 |
| AF11 | VSS41 | AM23 | VSS101 |
| AF12 | VSS42 | AM25 | VSS102 |
| AF14 | VSS43 | AM29 | VSS103 |
| AF16 | VSS44 | AM31 | VSS104 |
| AF18 | VSS45 | AM38 | VSS105 |
| AF23 | VSS46 | AM55 | VSS106 |
| AF25 | VSS47 | AN3 | VSS108 |
| AF29 | VSS48 | AN8 | VSS110 |
| AF40 | VSS51 | AN10 | VSS107 |
| AF42 | VSS52 | AN46 | VSS109 |

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UU1L
bga1090_25x24_1d45_gml
GLK SOC RVP2

| | | | |
|------|---------|------|---------|
| AN48 | VSS_111 | BC11 | VSS_165 |
| AN49 | VSS_112 | BC17 | VSS_166 |
| AN51 | VSS_113 | BC19 | VSS_167 |
| AN53 | VSS_114 | BC21 | VSS_168 |
| AP23 | VSS_115 | BC23 | VSS_169 |
| AP27 | VSS_116 | BC25 | VSS_170 |
| AP28 | VSS_117 | BC31 | VSS_171 |
| AP29 | VSS_118 | BC33 | VSS_172 |
| AP33 | VSS_119 | BC35 | VSS_173 |
| AP35 | VSS_120 | BC37 | VSS_174 |
| AR2 | VSS_124 | BC39 | VSS_175 |
| AR7 | VSS_130 | BC41 | VSS_176 |
| AR10 | VSS_121 | BC45 | VSS_177 |
| AR12 | VSS_122 | BC51 | VSS_179 |
| AR17 | VSS_123 | BD9 | VSS_187 |
| AR39 | VSS_125 | BD15 | VSS_180 |
| AR44 | VSS_126 | BD19 | VSS_181 |
| AR46 | VSS_127 | BD21 | VSS_182 |
| AR49 | VSS_128 | BD28 | VSS_183 |
| AR54 | VSS_129 | BD35 | VSS_184 |
| AT23 | VSS_131 | BD37 | VSS_185 |
| AJ39 | VSS_132 | C28 | VSS_186 |
| AU3 | VSS_135 | BE3 | VSS_189 |
| AK1 | VSS_133 | BE28 | VSS_188 |
| AU28 | VSS_134 | BE53 | VSS_190 |
| AU46 | VSS_136 | BF9 | VSS_194 |
| AU53 | VSS_137 | BF19 | VSS_191 |
| AV15 | VSS_138 | BF37 | VSS_192 |
| AV17 | VSS_139 | BF47 | VSS_193 |
| AV23 | VSS_140 | BG1 | VSS_195 |
| AV25 | VSS_141 | BG6 | VSS_199 |
| AV31 | VSS_142 | BG28 | VSS_196 |
| AV33 | VSS_143 | BG50 | VSS_197 |
| AV39 | VSS_144 | BG55 | VSS_198 |
| AV41 | VSS_145 | BH11 | VSS_200 |
| AW2 | VSS_147 | BH13 | VSS_201 |
| AW5 | VSS_150 | BH17 | VSS_202 |
| AW10 | VSS_146 | BH19 | VSS_203 |
| AW28 | VSS_148 | BH23 | VSS_204 |
| AW46 | VSS_149 | BH25 | VSS_205 |
| AW51 | VSS_151 | BH28 | VSS_206 |
| AW54 | VSS_152 | BH31 | VSS_207 |
| AY13 | VSS_153 | BH33 | VSS_208 |
| AY15 | VSS_154 | BH37 | VSS_209 |
| AY28 | VSS_155 | BH39 | VSS_210 |
| AY41 | VSS_156 | BH41 | VSS_211 |
| AY43 | VSS_157 | BH45 | VSS_212 |
| B2 | VSS_158 | BJ2 | VSS_215 |
| B55 | VSS_159 | BJ19 | VSS_216 |
| BA27 | VSS_160 | BJ25 | VSS_217 |
| BA29 | VSS_161 | BJ28 | VSS_218 |
| BB1 | VSS_162 | BJ31 | VSS_219 |
| BB28 | VSS_163 | BJ37 | VSS_220 |
| BB55 | VSS_164 | BJ41 | |
| BC5 | VSS_178 | | |

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UU1M
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GLK SOC RVP2

| | | |
|------|-------|--------|
| AL23 | VSS1 | J51 |
| BJ54 | VSS2 | K1 |
| BK1 | VSS3 | VSS53 |
| BK17 | VSS4 | VSS55 |
| BK21 | VSS5 | K28 |
| BK35 | VSS6 | K55 |
| BK39 | VSS7 | L5 |
| BK55 | VSS8 | L7 |
| BL5 | VSS17 | L8 |
| BL8 | VSS19 | L19 |
| BL10 | VSS9 | L33 |
| BL14 | VSS10 | M15 |
| BL24 | VSS11 | M25 |
| BL28 | VSS12 | M28 |
| BL32 | VSS13 | M35 |
| BL42 | VSS14 | M41 |
| BL46 | VSS15 | N12 |
| BL48 | VSS16 | N28 |
| BL51 | VSS18 | N46 |
| C1 | VSS20 | N51 |
| C12 | VSS21 | P21 |
| C16 | VSS22 | P55 |
| C28 | VSS23 | R8 |
| C36 | VSS24 | R28 |
| D6 | VSS25 | T27 |
| D9 | VSS26 | VSS75 |
| D21 | VSS27 | T38 |
| D28 | VSS28 | U13 |
| D41 | VSS29 | VSS78 |
| D45 | VSS30 | V27 |
| D55 | VSS31 | V38 |
| E28 | VSS32 | VSS81 |
| E50 | VSS33 | V55 |
| E55 | VSS34 | W2 |
| F1 | VSS35 | W3 |
| F4 | VSS36 | W5 |
| F21 | VSS37 | W7 |
| F31 | VSS38 | W8 |
| G28 | VSS39 | W10 |
| H13 | VSS40 | W39 |
| H15 | VSS41 | VSS83 |
| H21 | VSS42 | VSS86 |
| H23 | VSS43 | VSS87 |
| H28 | VSS44 | W41 |
| H33 | VSS45 | W43 |
| H39 | VSS46 | W44 |
| J8 | VSS47 | VSS89 |
| J27 | VSS48 | VSS90 |
| J33 | VSS49 | VSS91 |
| J41 | VSS50 | VSS92 |
| J45 | | VSS94 |
| | | VSS97 |
| | | VSS98 |
| | | VSS99 |
| | | VSS100 |
| | | Y21 |
| | | Y23 |
| | | Y25 |
| | | Y27 |
| | | Y31 |
| | | VSS101 |
| | | T3 |
| | | U3 |
| | | VSS79 |

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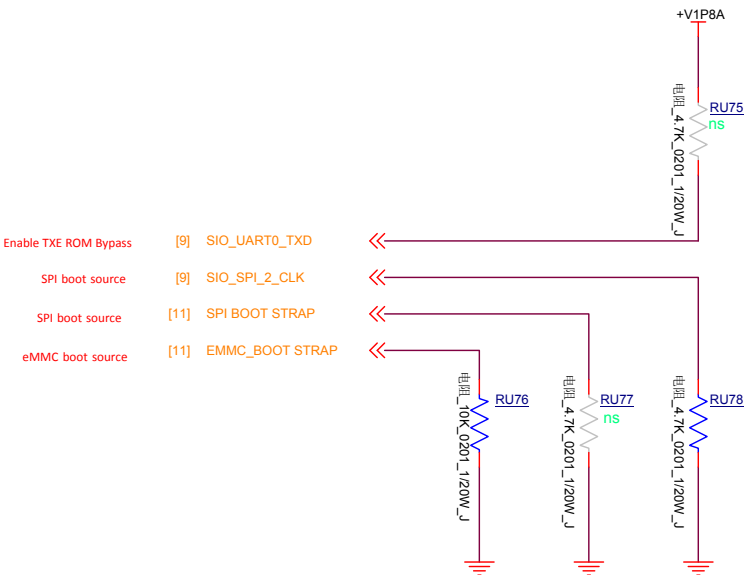
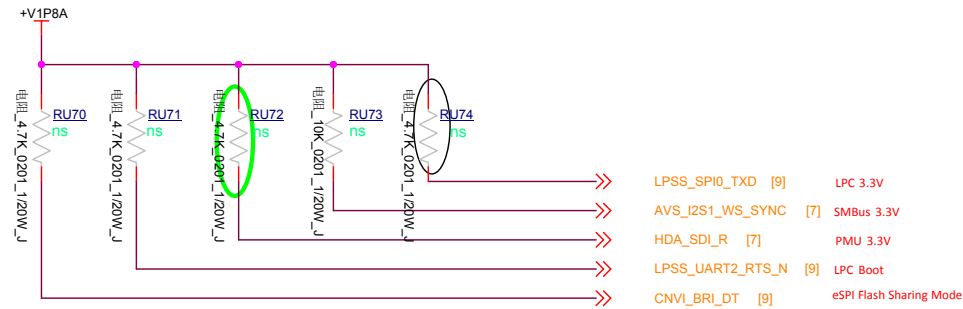
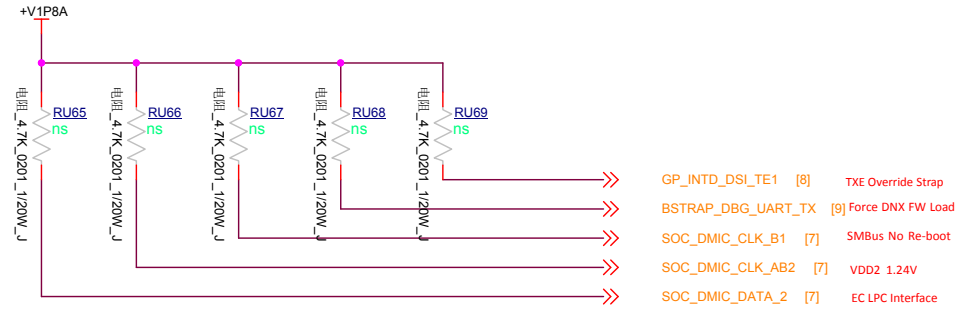
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Monday, May 07, 2018

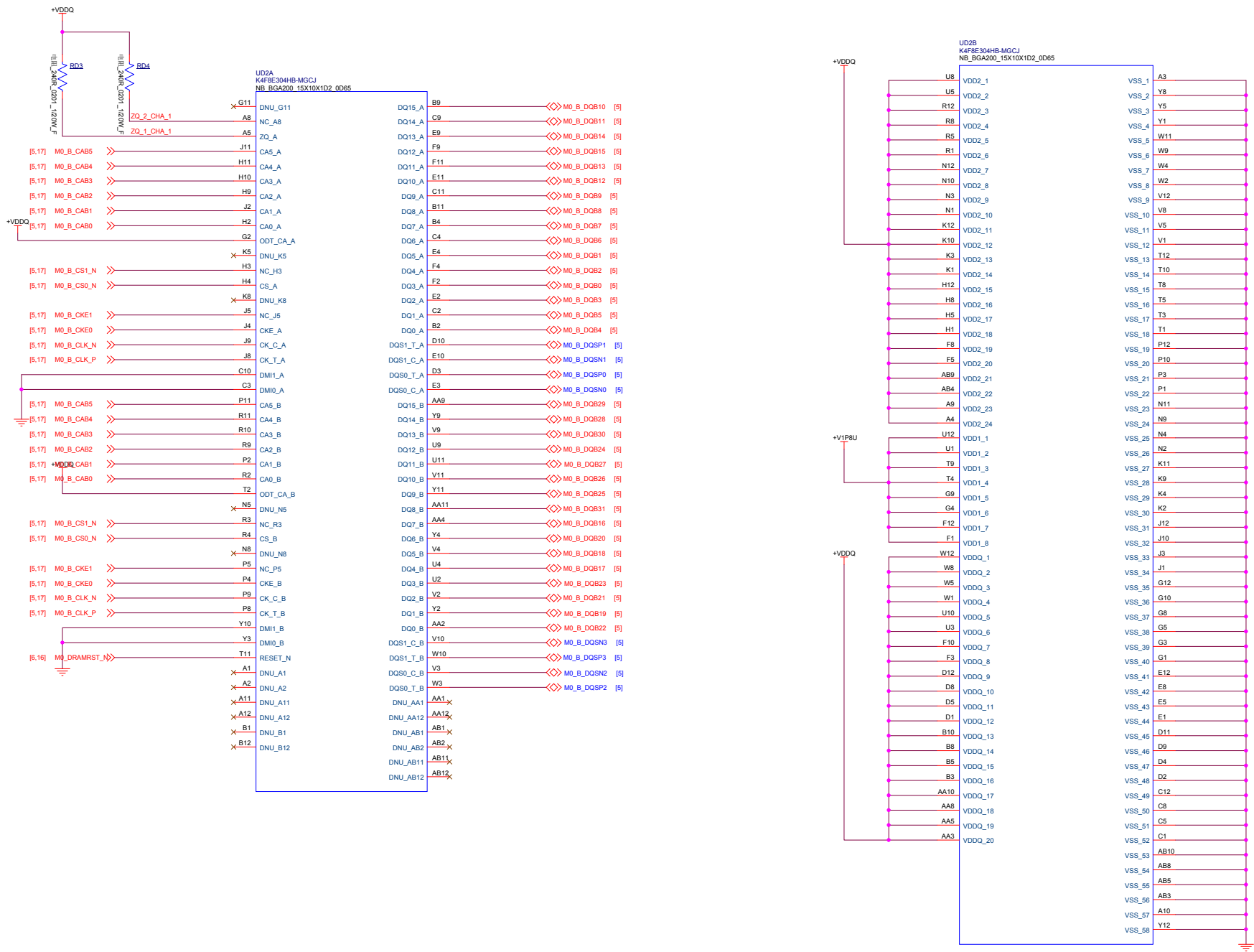
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|------------------|----------|-----------------------------|--------|---|
| LPSS_SPI0_TXD | GPIO_83 | LPC 1.8V/3.3V mode select | 20K PD | 1=buffers set to 1.8V mode 0=buffers set to 3.3V mode (default) |
| AVS_I2S1_WS_SYNC | GPIO_163 | SMBus 1.8V/3.3V mode select | 20K PD | 1=buffers set to 1.8V mode 0=buffers set to 3.3V mode (default) |
| HDA_SDI_R | GPIO_168 | PMU 1.8V/3.3V mode select | 20K PD | 1=buffers set to 1.8V mode 0=buffers set to 3.3V mode (default) |
| LPSS_UART2_RTS_N | GPIO_66 | LPC boot BIOS strap | 20K PD | 1=boot from LPC; 0=do not boot from LPC (default) |
| CNVI_BRI_DT | GPIO_191 | eSPI Flash Sharing Mode | 20K PD | eSPI Flash Sharing Mode: 1=slave attached flash sharing (SAFS); 0=master attached flash sharing (MAFS; default) |
| EMMC_BOOT STRAP | GPIO_27 | Allow eMMC as a boot source | 20K PU | 1=enable (default); 0=disable; |
| SPI BOOT STRAP | GPIO_28 | Allow SPI as a boot source | 20K PU | 1=enable (default) 0=disable |
| SIO_SPI_2_CLK | GPIO_84 | Allow SPI as a boot source | 20K PU | 1=disable 0=enable (default) |
| SIO_UART0_TXD | GPIO_61 | Enable TXE ROM Bypass | 20K PD | 1 = enable bypass 0 = disable bypass (default) |
| SOC_DMIC_DATA_2 | GPIO_175 | eSPI vs. LPC | 20K PD | 1=eSPI mode; 0=LPC mode (default) |
| SOC_DMIC_CLK_AB2 | GPIO_174 | VDD2 1.24V vs. 1.20V select | 20K PD | 1=VDD2 is 1.24V; 0=VDD2 is 1.20V (default) |
| SOC_DMIC_CLK_B1 | GPIO_172 | SMBus No Re Boot | 20K PD | 1 = Enable 0 = Disable (default) |
| LPSS_UART2_TXD | GPIO_65 | Force DNX FW Load | 20K PD | 1 = Force 0 = Do not force (default) |
| GP_INTD_DSI_TE1 | GPIO_42 | Flash Descriptor Override | 20K PD | 0 = No Override (Normal Operation) 1 = Override |

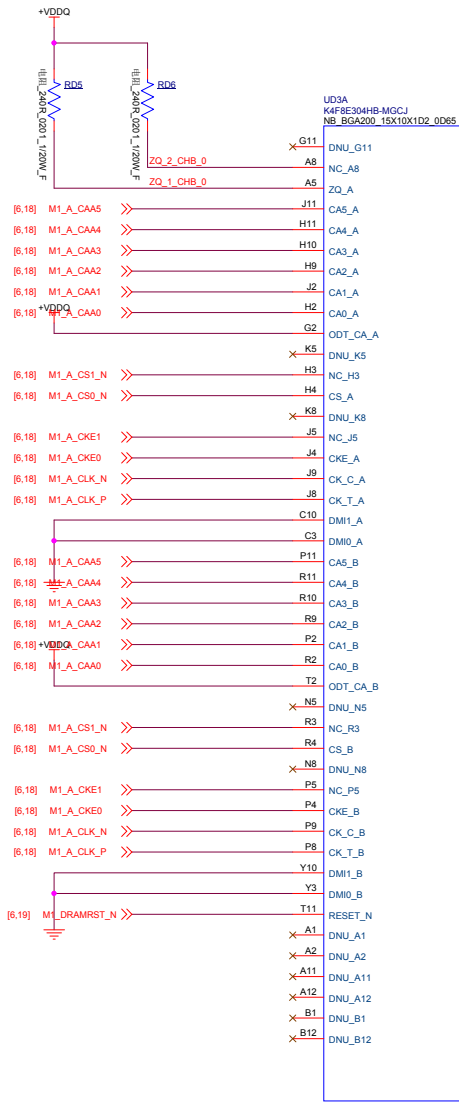


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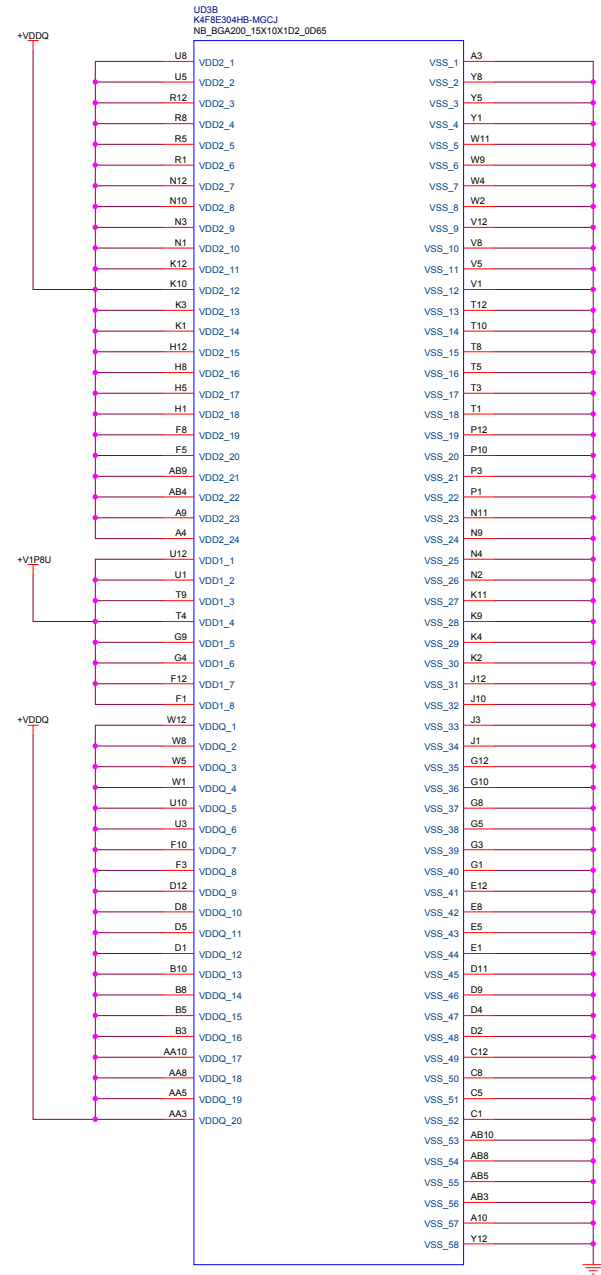
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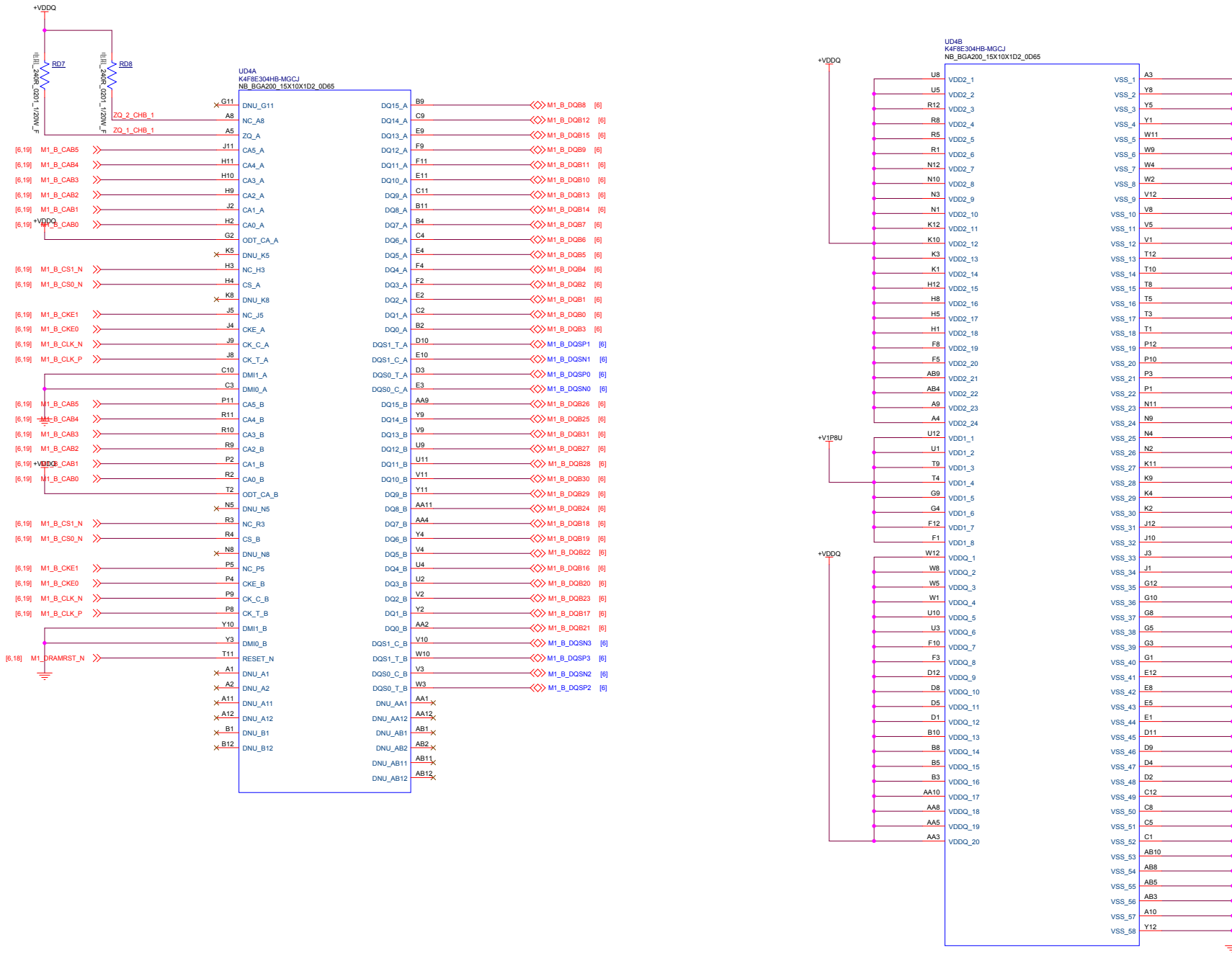
Huaqin Telecom Technology Com.,Ltd.



| | | | |
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| DQ15_A | B9 | ◇◇ M1_A_DQA29 | [6] |
| DQ14_A | C9 | ◇◇ M1_A_DQA27 | [6] |
| DQ13_A | E9 | ◇◇ M1_A_DQA25 | [6] |
| DQ12_A | F9 | ◇◇ M1_A_DQA26 | [6] |
| DQ11_A | F11 | ◇◇ M1_A_DQA28 | [6] |
| DQ10_A | E11 | ◇◇ M1_A_DQA24 | [6] |
| DQ9_A | C11 | ◇◇ M1_A_DQA31 | [6] |
| DQ8_A | B11 | ◇◇ M1_A_DQA30 | [6] |
| DQ7_A | B4 | ◇◇ M1_A_DQA19 | [6] |
| DQ6_A | C4 | ◇◇ M1_A_DQA18 | [6] |
| DQ5_A | E4 | ◇◇ M1_A_DQA22 | [6] |
| DQ4_A | F4 | ◇◇ M1_A_DQA21 | [6] |
| DQ3_A | F2 | ◇◇ M1_A_DQA20 | [6] |
| DQ2_A | E2 | ◇◇ M1_A_DQA23 | [6] |
| DQ1_A | C2 | ◇◇ M1_A_DQA16 | [6] |
| DQ0_A | B2 | ◇◇ M1_A_DQA17 | [6] |
| DQS1_T_A | D10 | ◇◇ M1_A_DQSP3 | [6] |
| DQS1_C_A | E10 | ◇◇ M1_A_DQSN3 | [6] |
| DQS0_T_A | D3 | ◇◇ M1_A_DQSP2 | [6] |
| DQS0_C_A | E3 | ◇◇ M1_A_DQSN2 | [6] |
| DQ15_B | AA9 | ◇◇ M1_A_DQA1 | [6] |
| DQ14_B | Y9 | ◇◇ M1_A_DQA0 | [6] |
| DQ13_B | V9 | ◇◇ M1_A_DQA2 | [6] |
| DQ12_B | U9 | ◇◇ M1_A_DQA3 | [6] |
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| DQ9_B | Y11 | ◇◇ M1_A_DQA8 | [6] |
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| DQ7_B | AA4 | ◇◇ M1_A_DQA15 | [6] |
| DQ6_B | Y4 | ◇◇ M1_A_DQA12 | [6] |
| DQ5_B | V4 | ◇◇ M1_A_DQA13 | [6] |
| DQ4_B | U4 | ◇◇ M1_A_DQA14 | [6] |
| DQ3_B | U2 | ◇◇ M1_A_DQA11 | [6] |
| DQ2_B | V2 | ◇◇ M1_A_DQA8 | [6] |
| DQ1_B | Y2 | ◇◇ M1_A_DQA9 | [6] |
| DQ0_B | AA2 | ◇◇ M1_A_DQA10 | [6] |
| DQS1_C_B | V10 | ◇◇ M1_A_DQSN0 | [6] |
| DQS1_T_B | W10 | ◇◇ M1_A_DQSP0 | [6] |
| DQS0_C_B | V3 | ◇◇ M1_A_DQSN1 | [6] |
| DQS0_T_B | W3 | ◇◇ M1_A_DQSP1 | [6] |
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| DNU_AA12 | AA12 | ✗ | |
| DNU_AB1 | AB1 | ✗ | |
| DNU_AB2 | AB2 | ✗ | |
| DNU_AB11 | AB11 | ✗ | |
| DNU_AB12 | AB12 | ✗ | |



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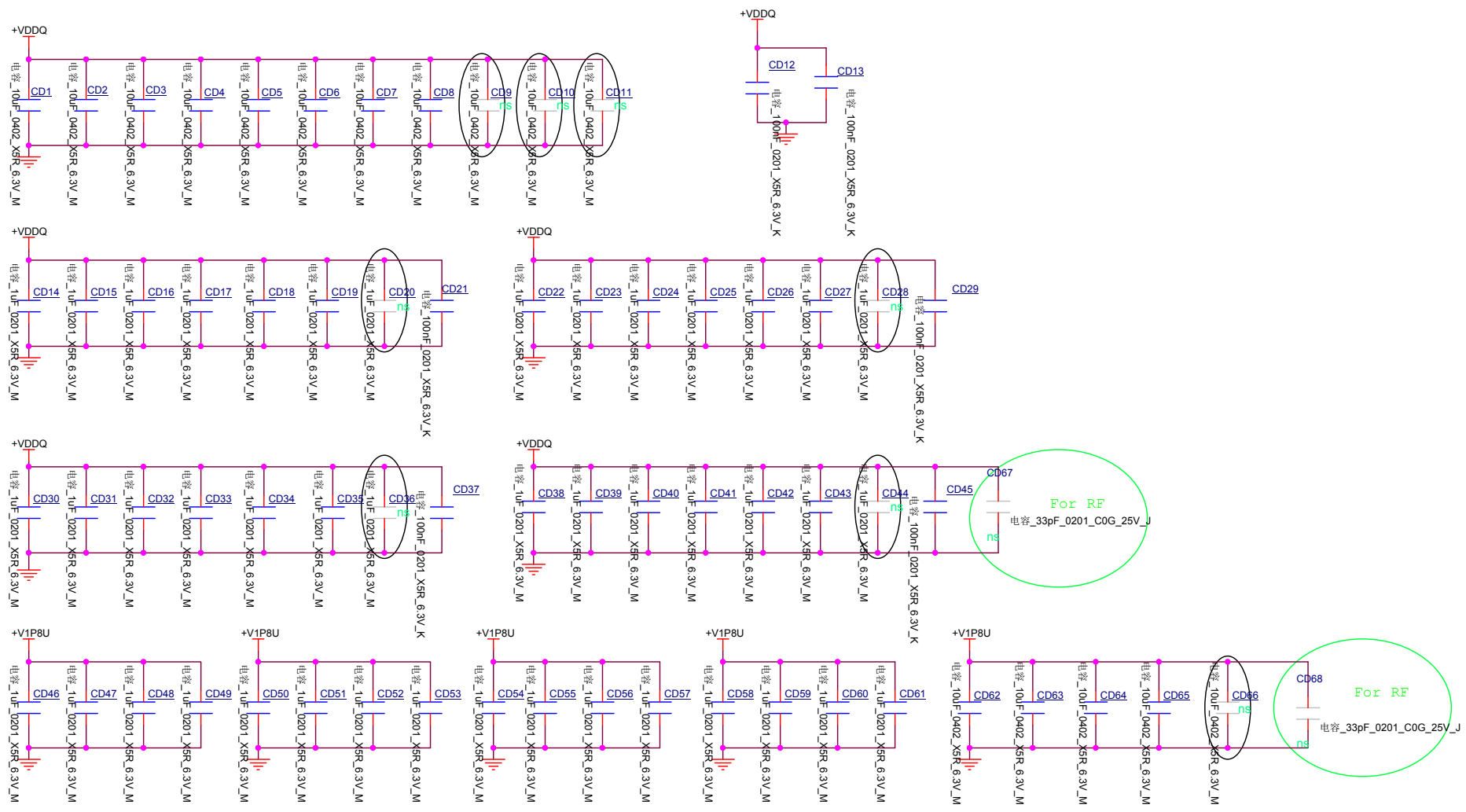
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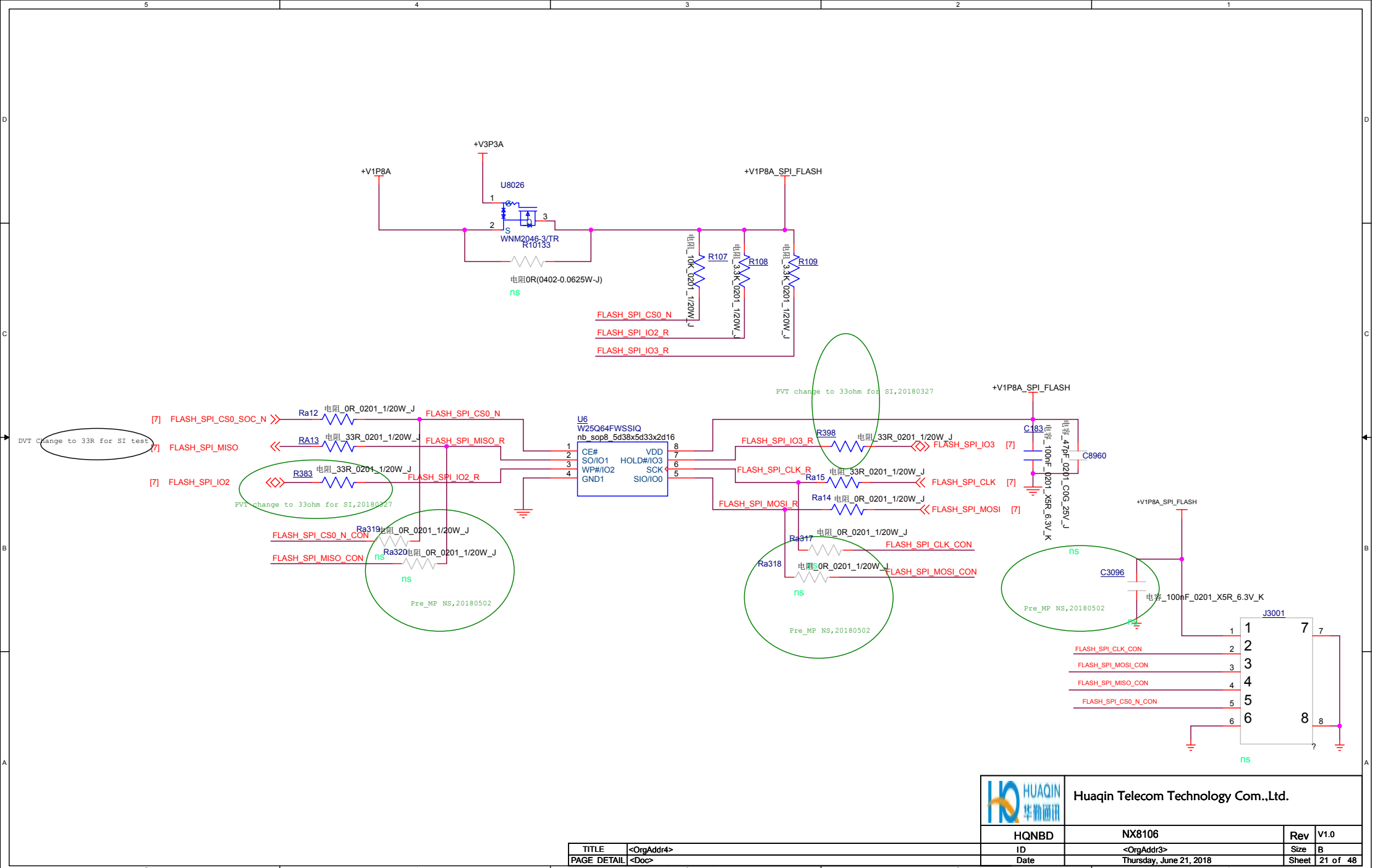


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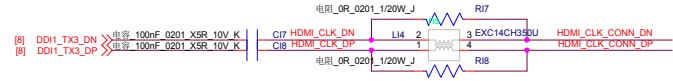
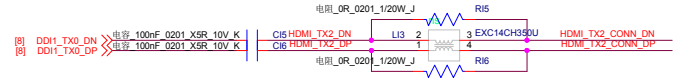
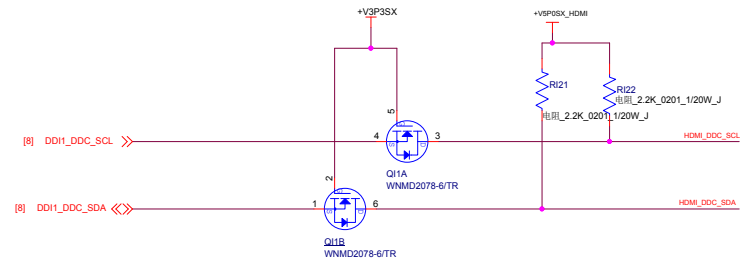
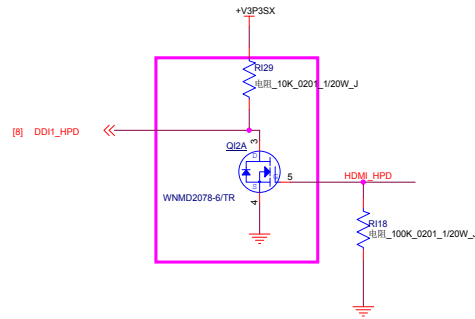
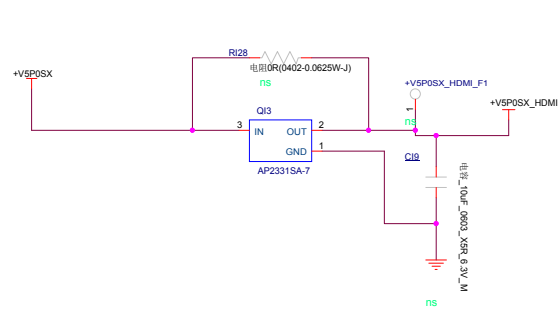
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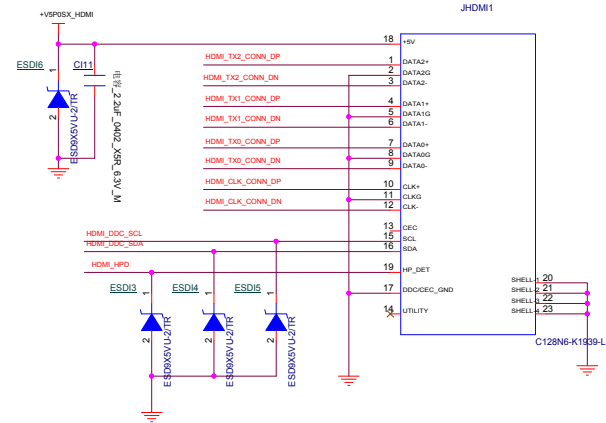
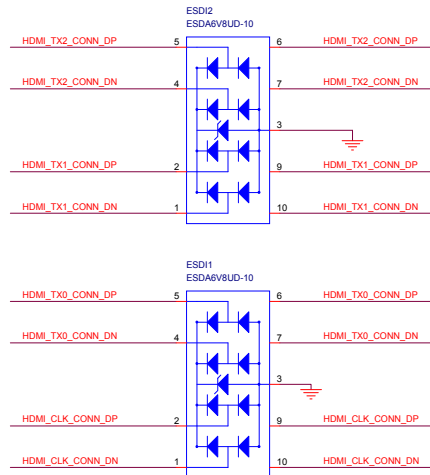
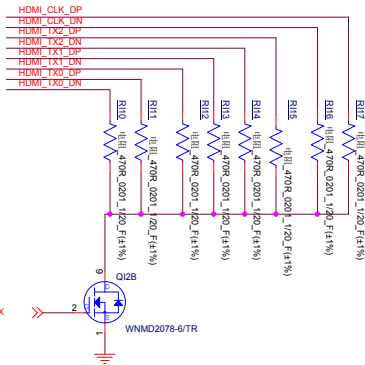


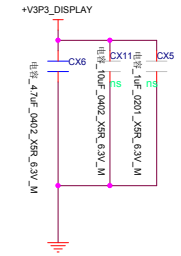
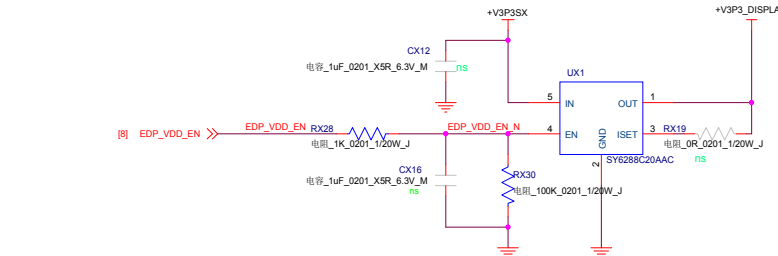
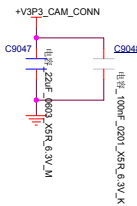
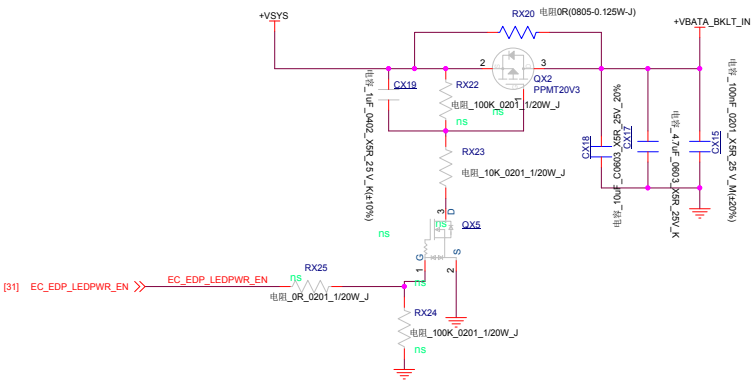
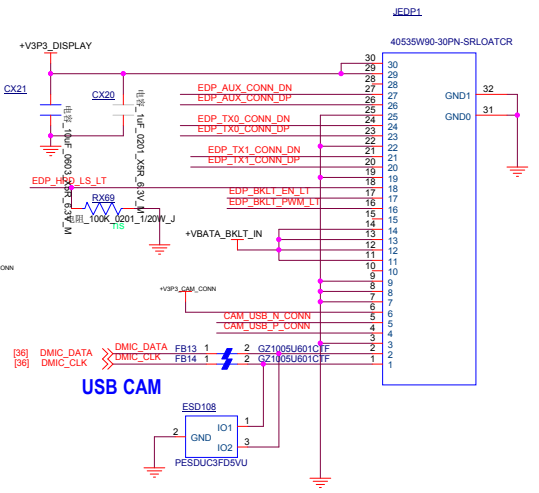
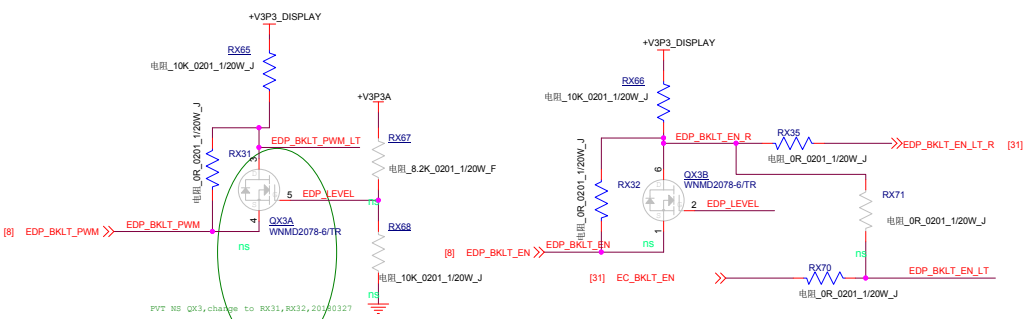
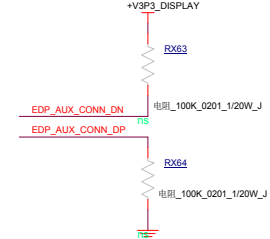
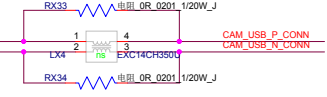
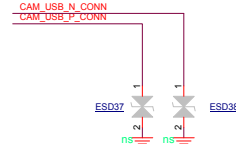
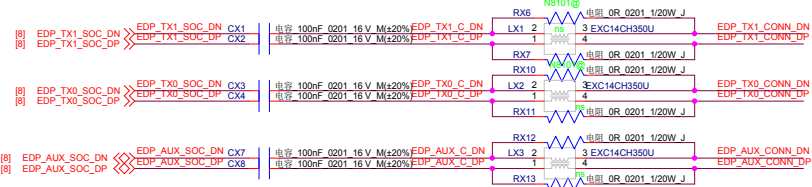
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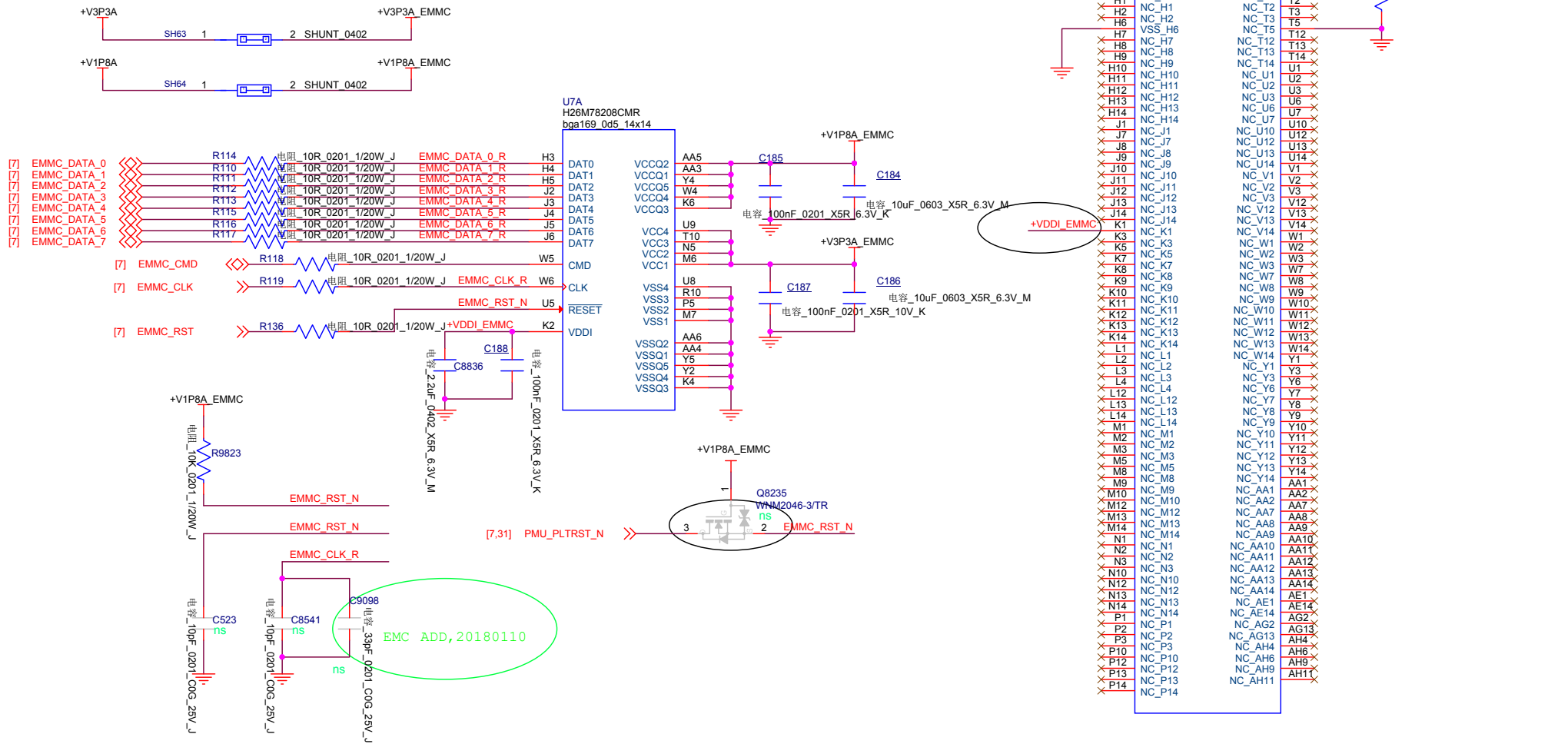
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DDI PIN reversed,not need reverce at CPU side

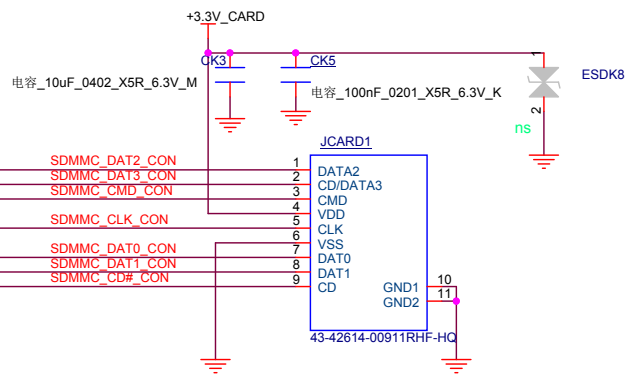
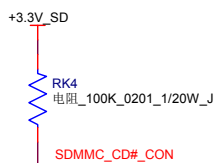
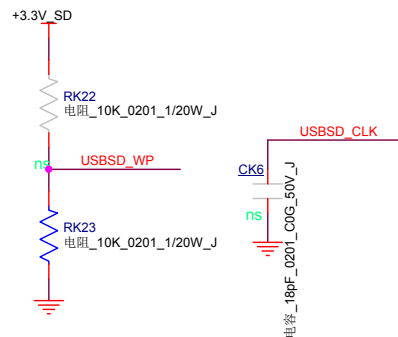
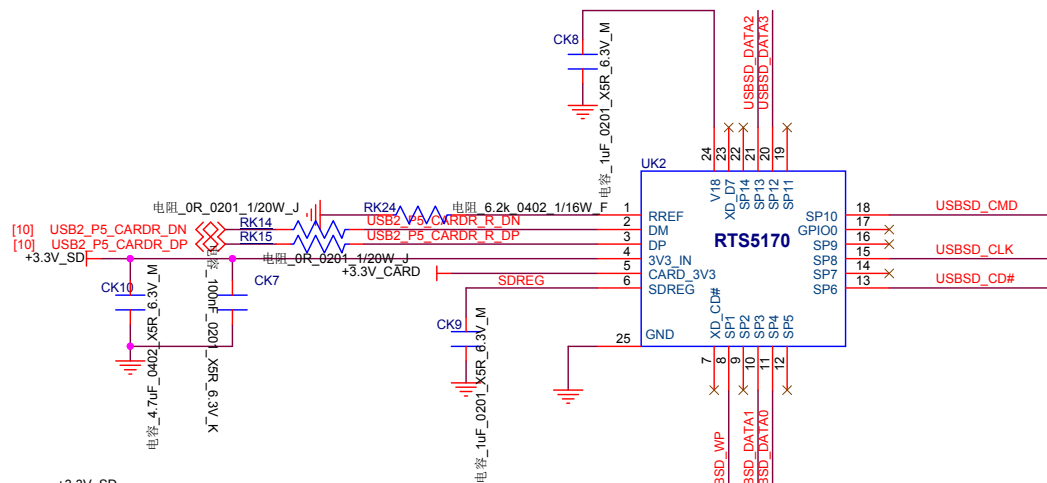
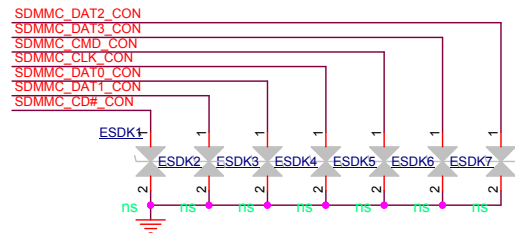
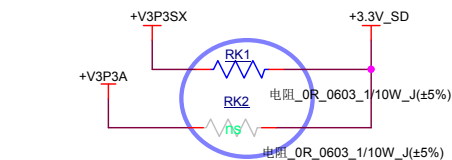





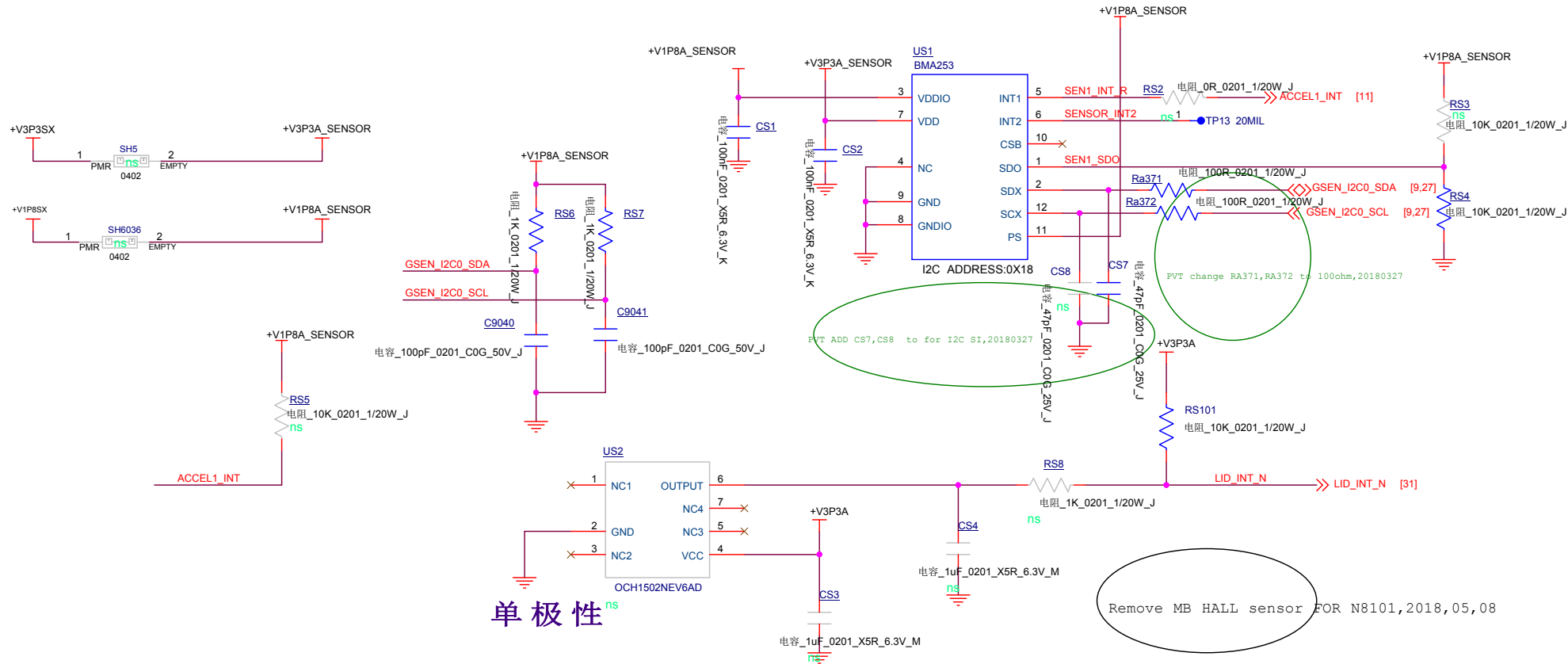



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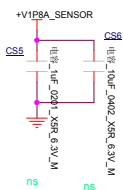


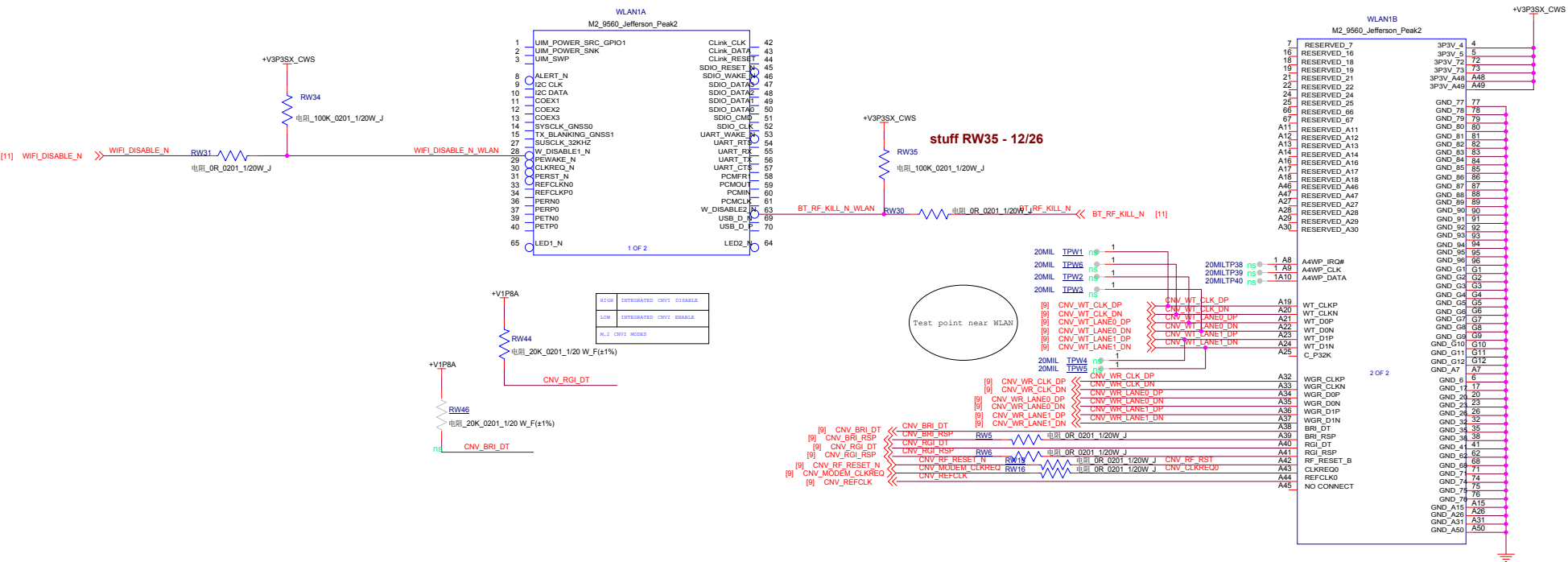
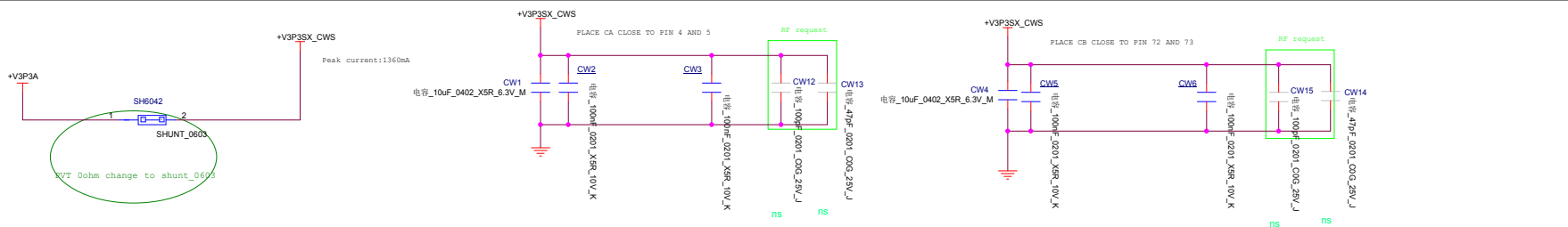
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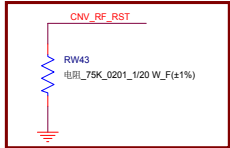
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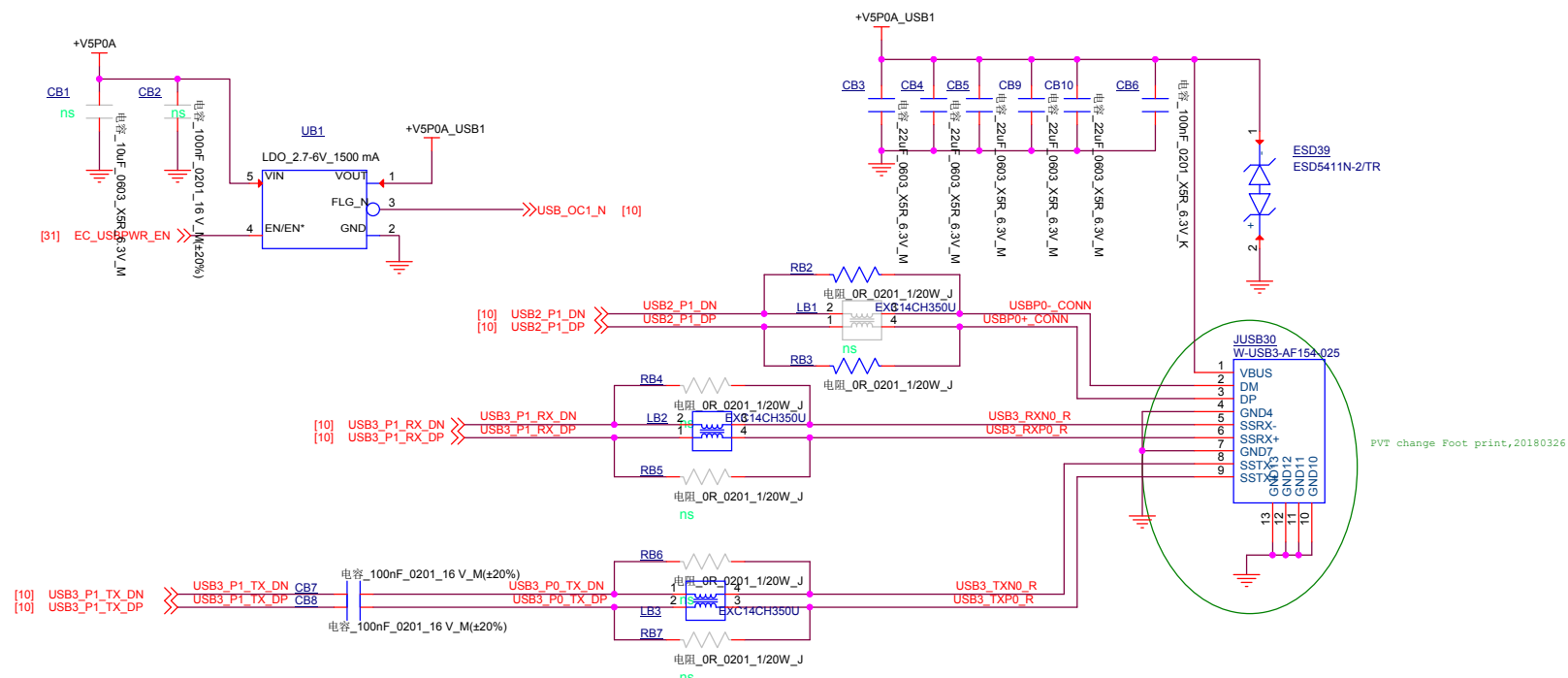
CAD NOTE: PLACE WLAN PCIE TX CAPS CLOSE TO PROCESSOR PINS



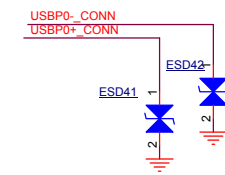
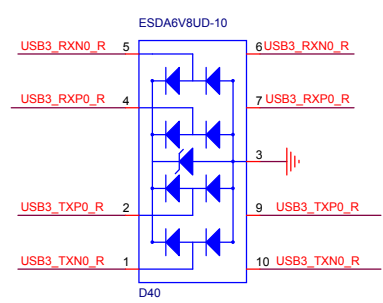
1.add RW43 RES - 12/26
2.change RW43 from 71.5K to 75K - 0104




NC RW36/RW37 RES - 12/26

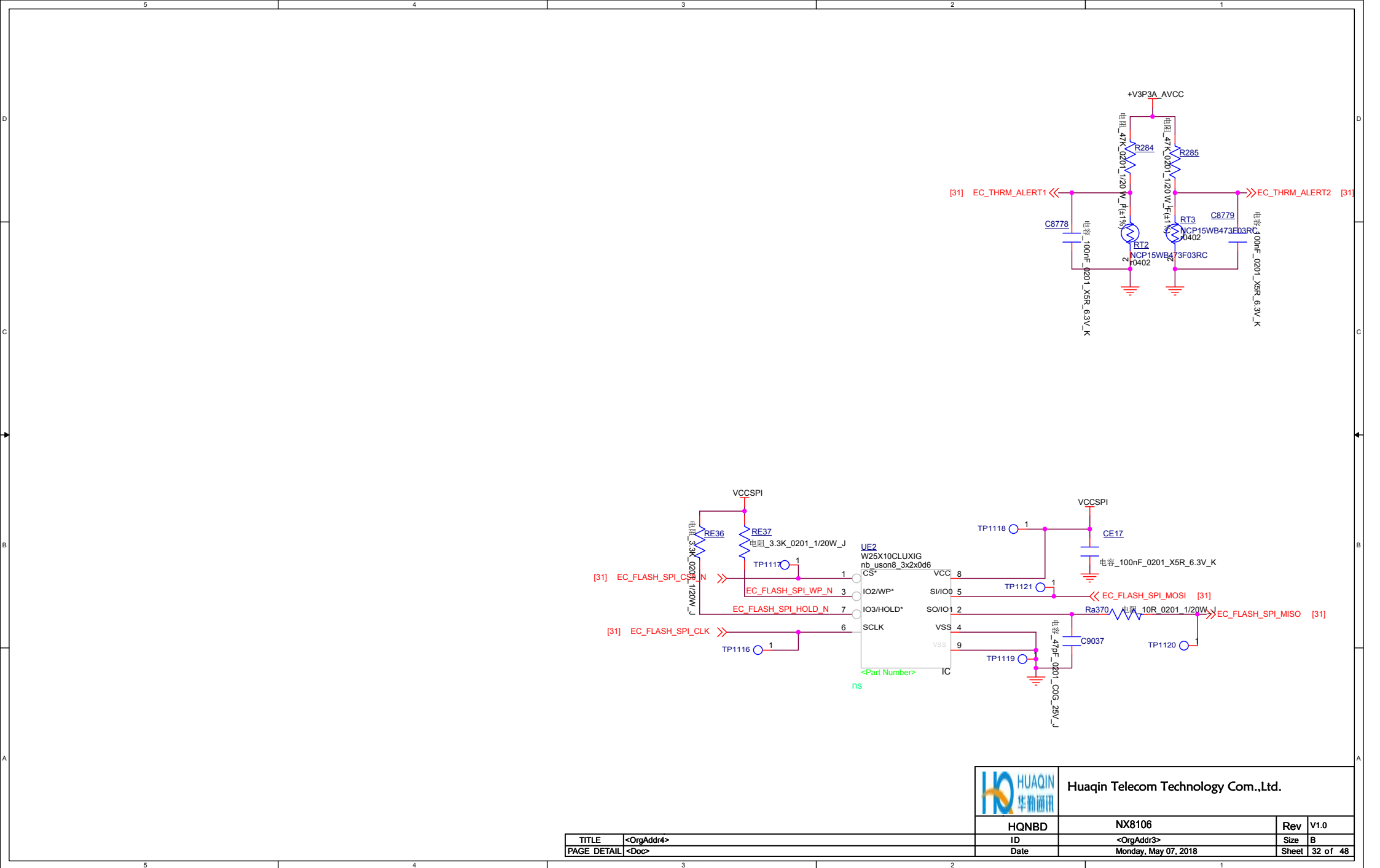


NEED TO CHANGE TO RT9742



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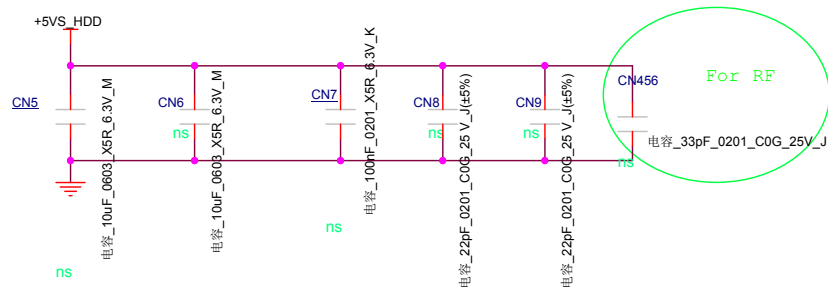
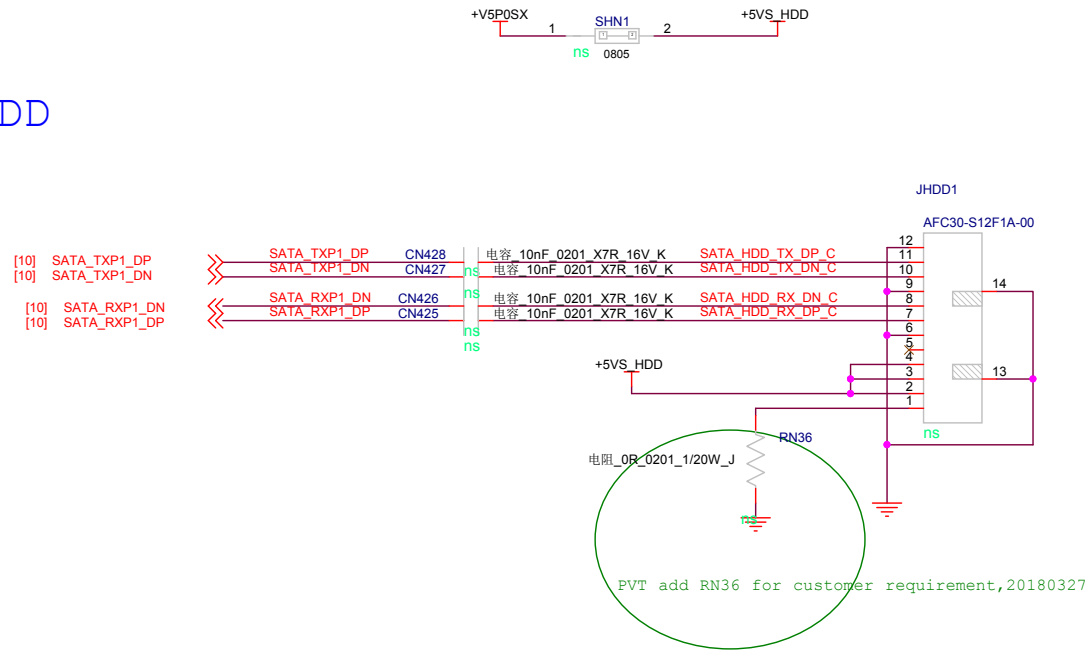
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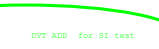
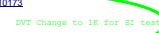
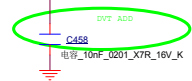
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HDD



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| HQNBD | NX8106 | Rev | V1.0 |
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PVT Change PCB Foot print
20180326


PVT Change PCB Foot print
20180326

PreMP NS, 20180502

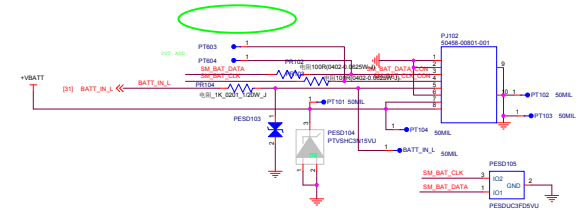
Charger LED

Reserver for N8101

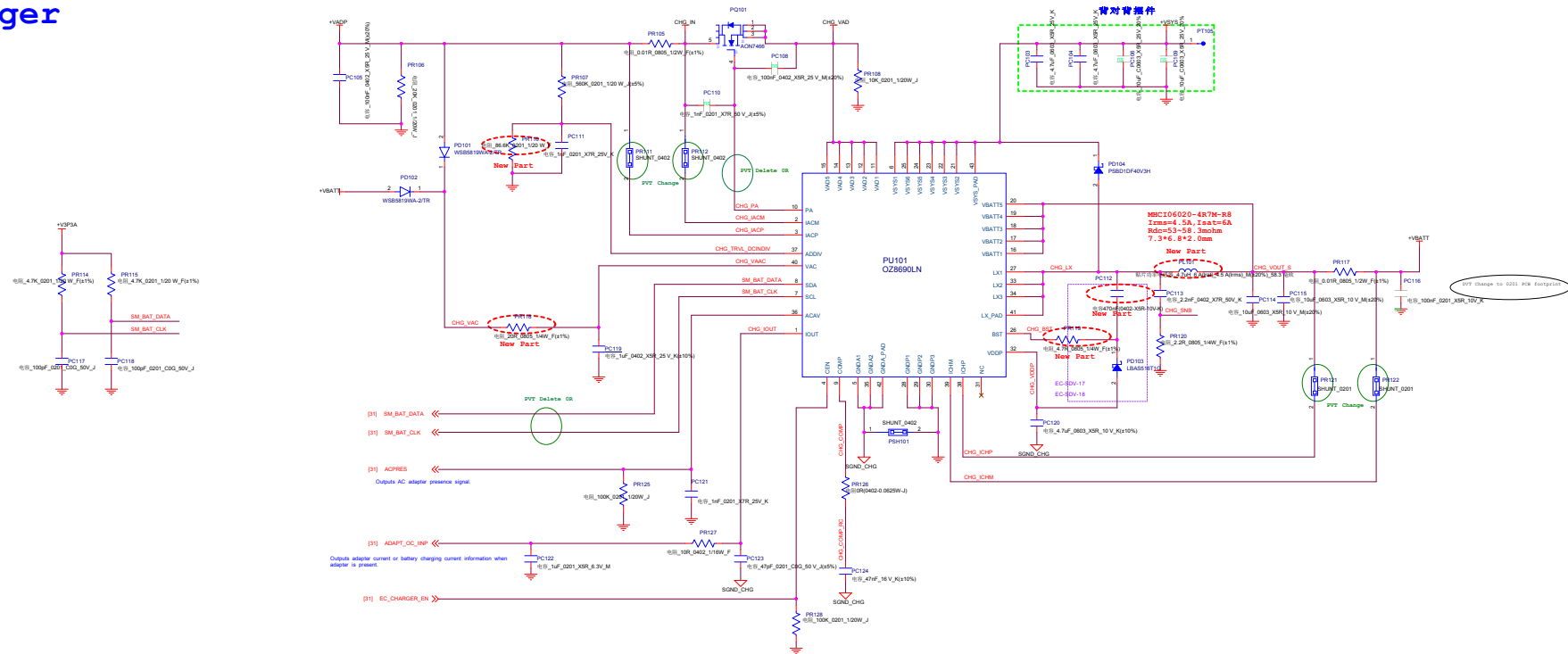
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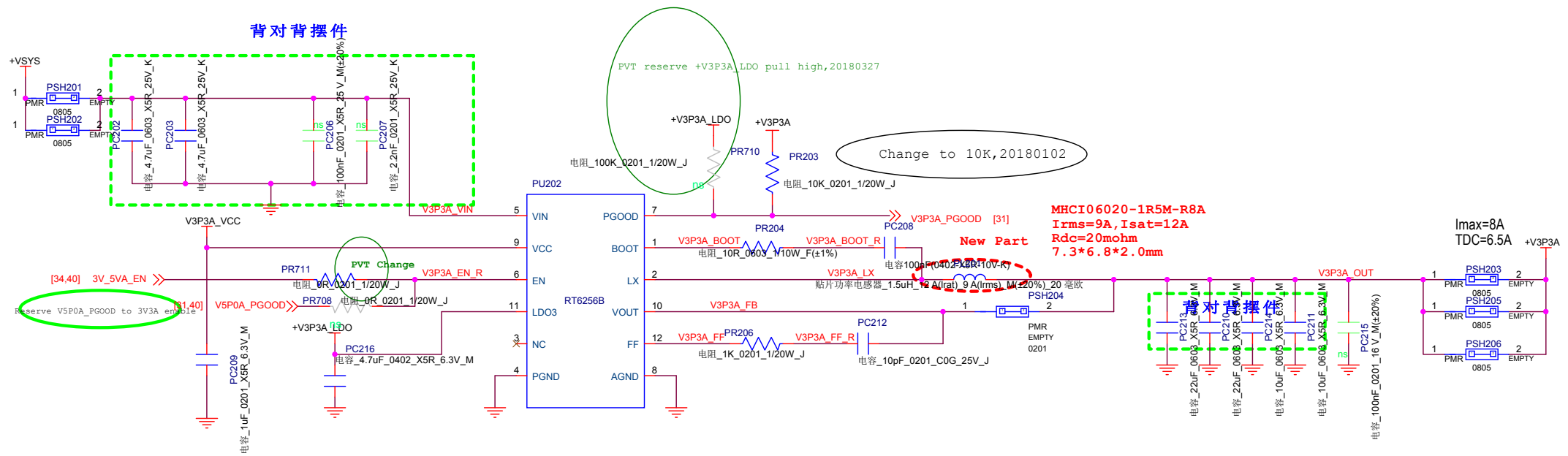
Battery CONN



Charger



+V3P3A



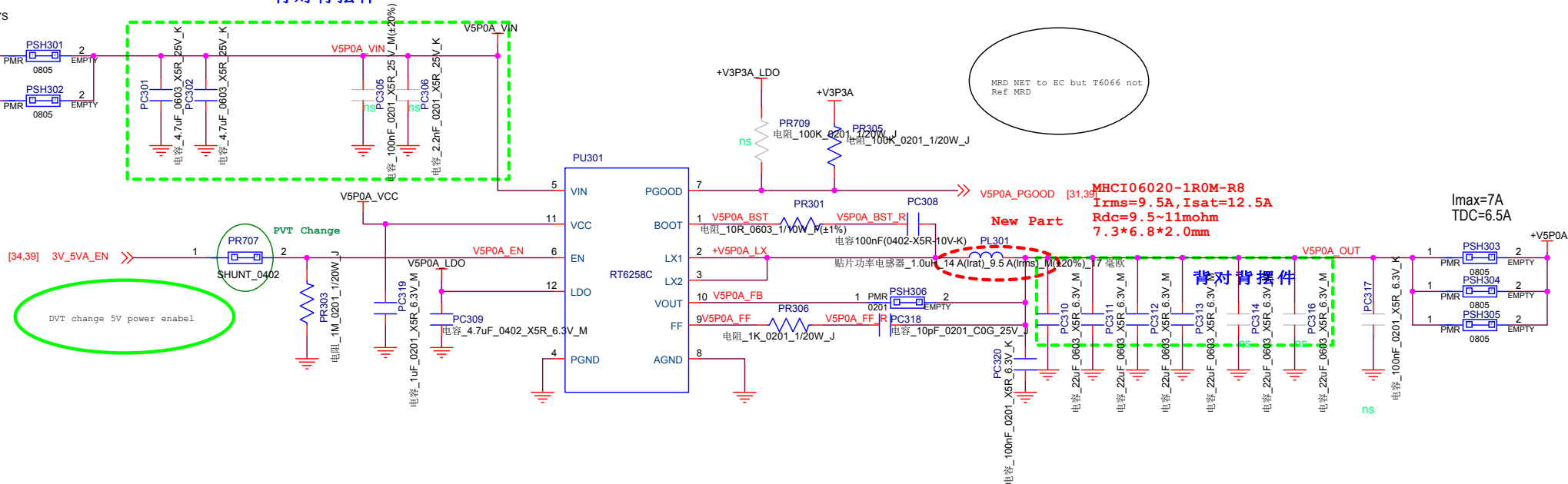
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+V5P0A

背对背摆件



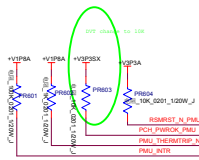
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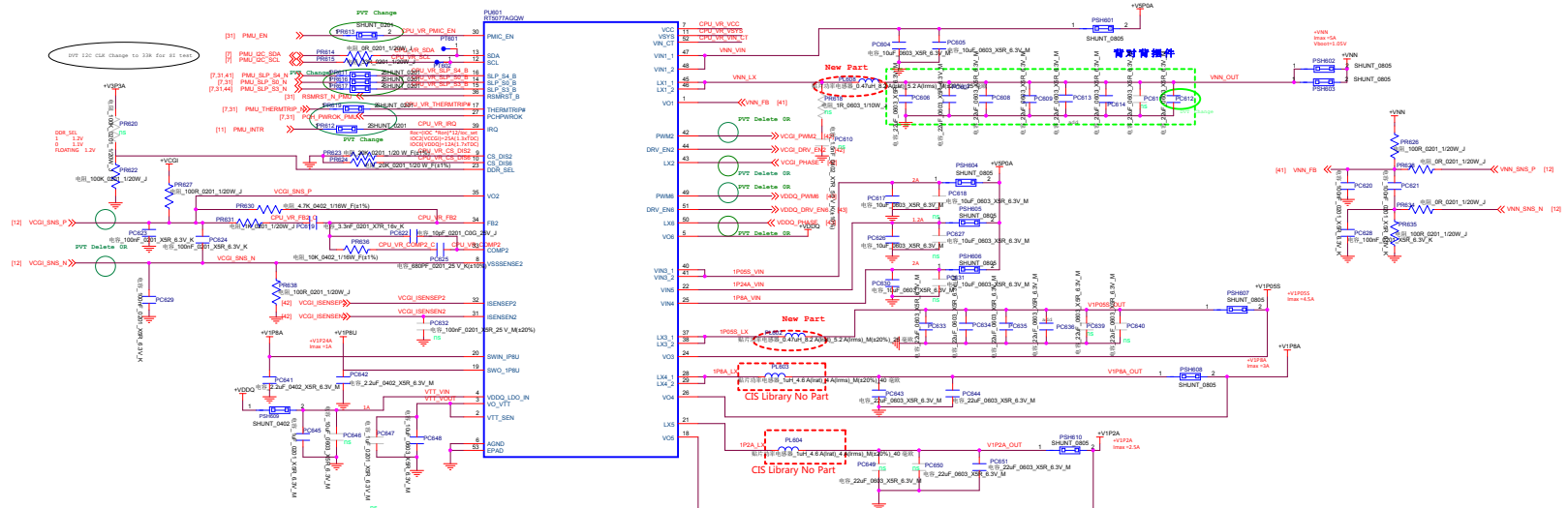
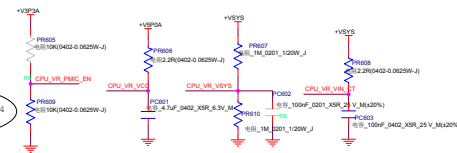
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NX8106
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Thursday, June 21, 2018

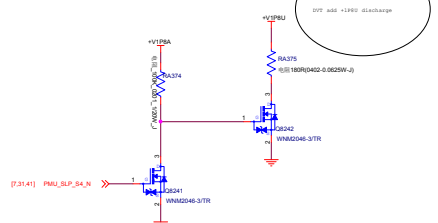
Rev V1.0
Size B
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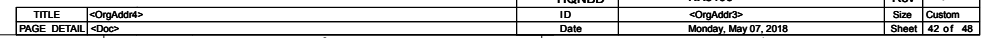


DVT stuff for 'VNN,20180224'

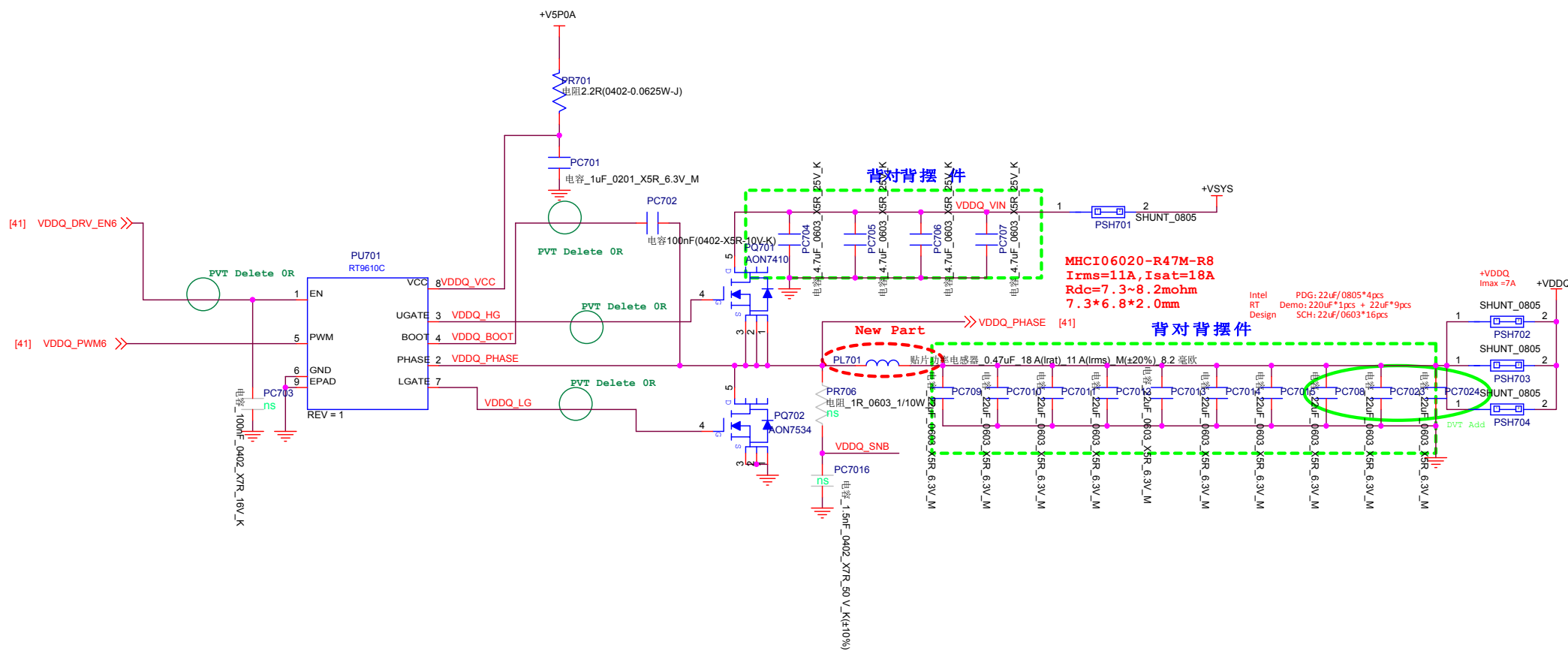


PVT add +1.00V discharge





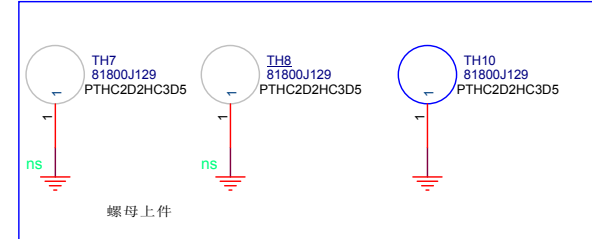
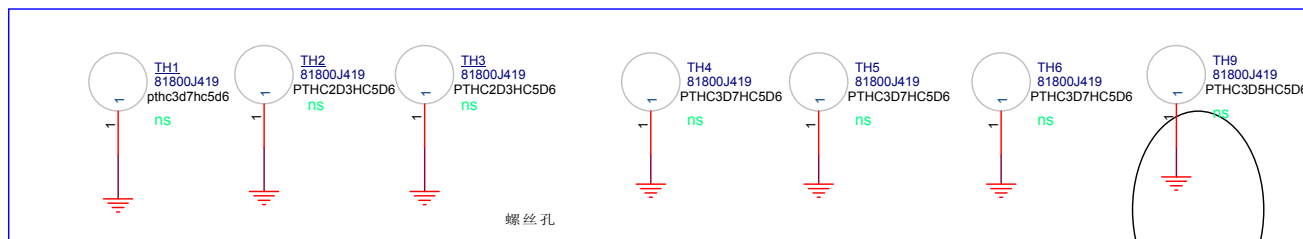
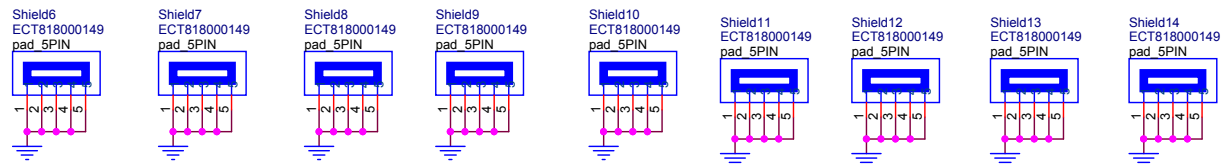
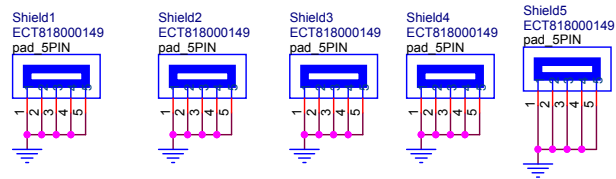
VDDQ



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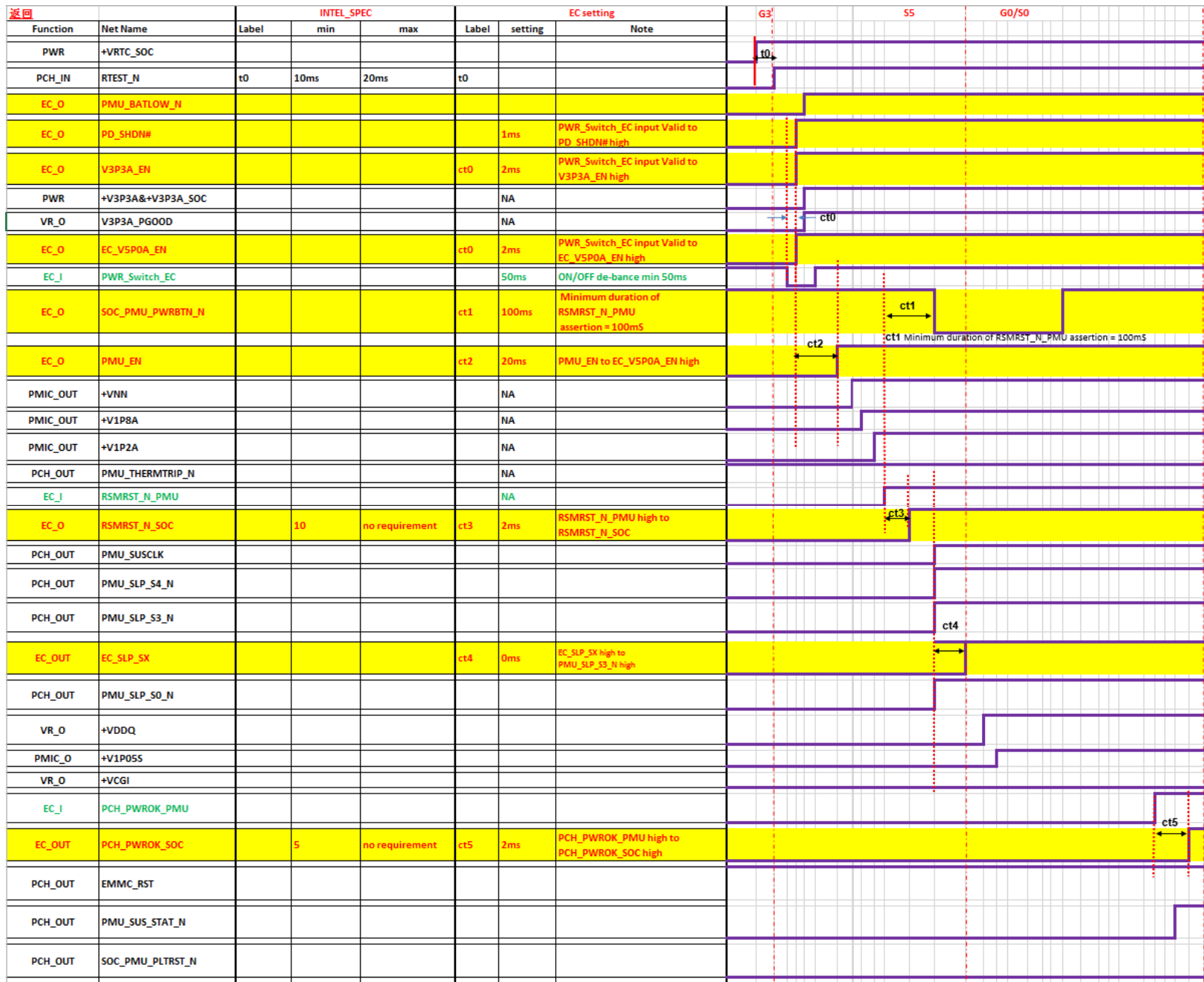
Pre_MP change PCB foot print,NS,20180423



Huaqin Telecom Technology Com.,Ltd.


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| 返回 | INTEL_SPEC | | | EC setting | | | G3 | S5 | S4/S3 |
|----------|--------------------|---------|------|------------|-------|---------|--|-----|--|
| Function | Net Name | Label | min | max | Label | setting | Note | | |
| PWR | +VRTC_SOC | | | | | NA | | t0 | t0: VCC_RTC_3P3V power to RTC_TEST_N/SRTCST_N ramped |
| PCH_IN | RTEST_N | t0 | 10ms | 20ms | | Auto | | | |
| EC_I | ADP_PRESENT | | | | | NA | | | |
| EC_O | PMU_AC_PRESENT | | | | | 1ms | ADP_PRESENT high to PMU_AC_PRESENT high | | |
| EC_O | PD_SHDN# | | | | | 1ms | ADP_PRESENT high to PD_SHDN# high | | |
| EC_O | V3P3A_EN | | | | | 2ms | ADP_PRESENT high to V3P3A_EN high | | |
| PWR | +V3P3A#&+V3P3A_SOC | | | | | NA | | | |
| VR_O | V3P3A_PGOOD | | | | | NA | | | |
| EC_O | EC_V5P0A_EN | | | | | 2ms | ADP_PRESENT high to EC_V5P0A_EN | | |
| EC_I | PWR_Switch_EC | | | | | 50ms | ON/OFF de-bance min 50ms | | |
| EC_O | SOC_PMU_PWRBTN_N | | | | ct1 | 100ms | Minimum duration of RSMRST_N_PMU assertion = 100ms | ct1 | ct1 Minimum duration of RSMRST_N_PMU assertion = 100ms |
| EC_O | PMU_EN | | | | ct2 | 6ms | PWR_Switch_EC delay 1ms | ct2 | |
| PMIC_OUT | +VNN | | | | | NA | | | |
| PMIC_OUT | +V1P8A | | | | | NA | | | |
| PMIC_OUT | +V1P2A | | | | | NA | | | |
| PCH_OUT | PMU_THERMTRIP_N | | | | | | | | |
| EC_I | RSMRST_N_PMU | | | | | | | | |
| EC_O | RSMRST_N_SOC | ct3 | 10 | no require | ct3 | 2ms | RSMRST_N_PMU high to RSMRST_N_SOC high | ct3 | |
| PCH_OUT | PMU_SUSCLK | | | | | | | | |
| PCH_OUT | PMU_SLP_S4_N | | | | | | | | |
| PCH_OUT | PMU_SLP_S3_N | | | | | | | | |
| EC_OUT | EC_SLP_SX | | | | ct4 | 0ms | EC_SLP_SX high to PMU_SLP_S3_N high | ct4 | |
| PCH_OUT | PMU_SLP_S0_N | | | | | | | | |
| VR_O | +VDDQ | | | | | | | | |
| PMIC_O | +V1P05S | | | | | | | | |
| VR_O | +VCGI | | | | | | | | |
| EC_I | PCH_PWROK_PMU | | | | | | | | |
| EC_OUT | PCH_PWROK_SOC | t11,t13 | 5 | no require | ct5 | 2ms | PCH_PWROK_PMU high to PCH_PWROK_SOC high | ct5 | |
| PCH_OUT | EMMC_RST | | | | | | | | |
| PCH_OUT | PMU_SUS_STAT_N | | | | | | | | |
| PCH_OUT | SOC_PMU_PLTRST_N | | | | | | | | |

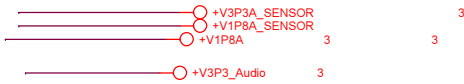


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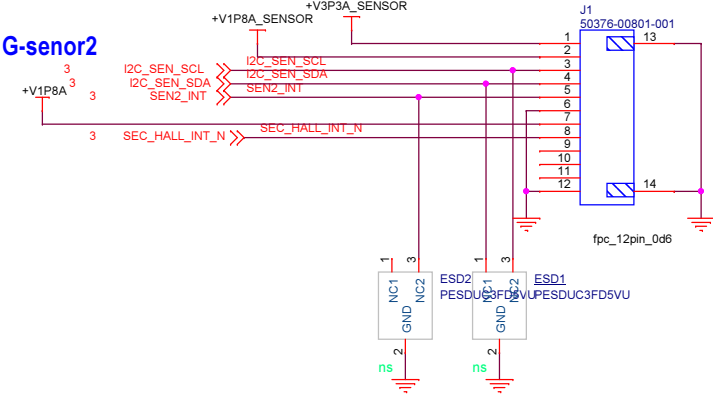
1.Add Battery SMBUS test point,PT603,PT604.----20180131
2.JHDD1 PIN4&PIN5 NC.----20180131
3.Memory ID table order from ID0,ID1,ID2 to ID2,ID1,ID0--20180202

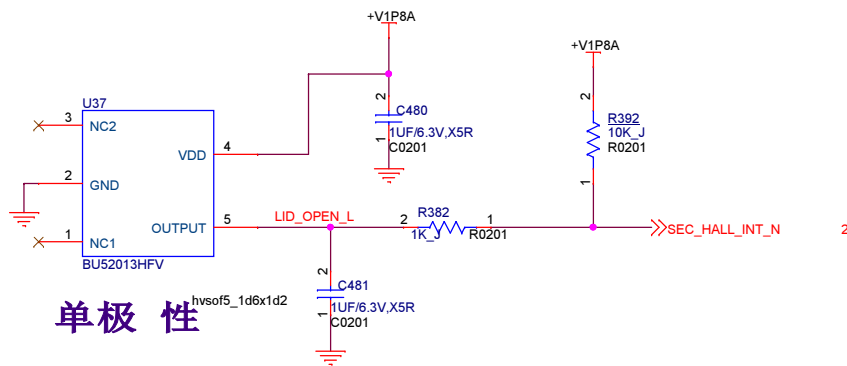
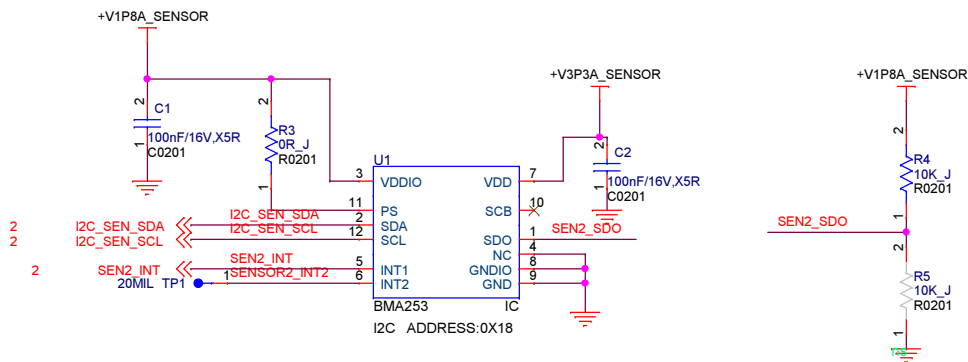
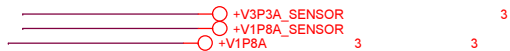
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| HQNBD | NX8106 | | | Rev | V1.0 |
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| Date | Monday, May 07, 2018 | | | Sheet | 48 of 48 |


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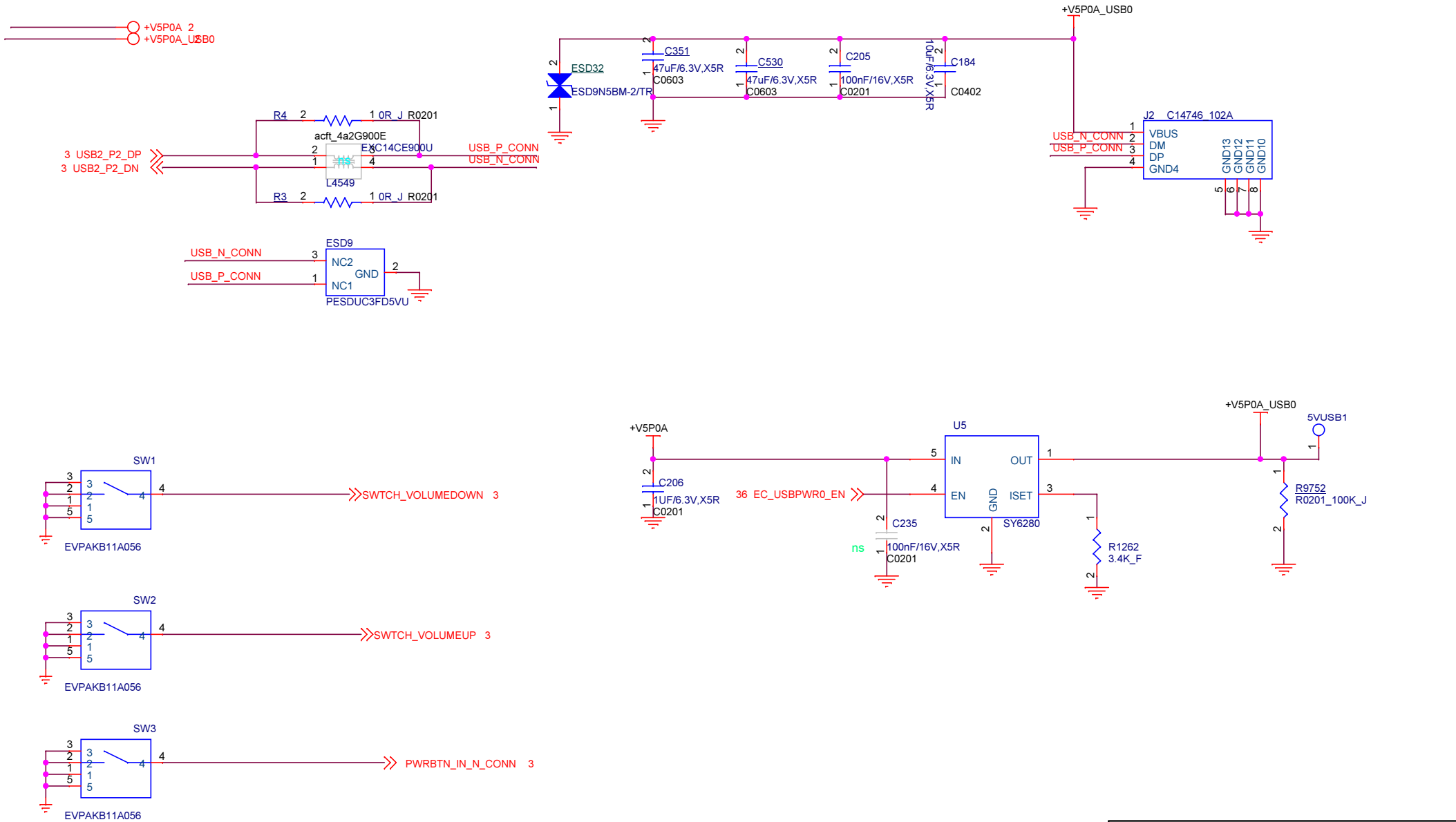



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|  | | Huaqin Telecom Technology Com.,Ltd. | |
| Page name: G Sensor/Holl Sensor | | | |
| Size: A4 | Project Name: N8200 | REV: V1.0 | |
| Date: Friday, January 06, 2017 | Sheet: 3 | of 5 | |



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|---|--------------------------|--|--------------|
|  HUAQIN 华勤通讯 | | Huaqin Telecom Technology Com.,Ltd. | |
| Page name: | | USB2.0 | |
| Size: | A4 | Project Name: | N8101 |
| Date: | Saturday, April 01, 2017 | REV: | V1.0 |
| Sheet: | | of | 3 |

